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The PR2D (Place, Route in 2-Dimensions) Automatic Layout Computer Program Handbook

Teddy M. Edge

SEPTEMBER 1978

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### The PR2D (Place, Route in 2-Dimensions) Automatic Layout Computer Program Handbook

Teddy M. Edge George C. Marshall Space Flight Center Marshall Space Flight Center, Alabama



Scientific and Technical Information Office

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## THE PR2D (PLACE, ROUTE IN 2-DIMENSIONS) AUTOMATIC LAYOUT COMPUTER PROGRAM HANDBOOK

#### SUMMARY

Place, Route in 2-Dimensions (PR2D) is a standard cell automatic layout computer program for generating Large Scale Integrated/Metal Oxide Semiconductor (LSI/MOS) arrays, and is one of the software components of the NASA/MSFC Computer Aided Design and Test system (CADAT). The PR2D program source is available from the COSMIC software distribution system.

This material describes the compilation, loading, and execution of the program on a Sigma V CP-V operating system located at NASA/MSFC. This material can also be used as a guide in the conversion and execution of the program at other facilities even if equipped with different data processing systems.

The PR2D computer program is written in FORTRAN IV and consists of 50 FORTRAN source routines including the main program.

One source routine of importance is the input/output routine, which is usually unique to each installation. This routine was called TAPES in the original RCA source but was modified and renamed IOSCR for the Sigma V CP-V installation. The commented source for the routine can be used as a guide in creating an input/output routine for a particular system.

### I. COMPILING AND LOADING PR2D

With the CP-V FORTRAN, large programs can only be compiled sequentially from tape, not from disc. Therefore, it is easier to edit the total source (approximately 30 000 card images) on a disc file and then compile and load the program with a batch job that copies the PR2D source file to tape and then compiles from the tape. Loading can be done in the same job or with a separate batch loading job.

Individual routines can be modified by copying them from the total source file into separate source files and editing them individually. The separate source files can be compiled on-line to generate the object modules. A batch load run is made to include the new rather than the old object modules. This procedure works well if changes are limited to a few routines.

### II. FLOWCHARTS OF THE MAJOR PR2D FUNCTIONS

Flowcharts of the major functions MAIN (program executive), PLACE (executive routine PLEX 1), ROUTE (executive routine RTEX1), and ARTWORK (executive routine ARTEX1) of the PR2D program are included for aid in understanding or making modifications to the program. The flowcharts for MAIN, PLACE, ROUTE, and ARTWORK are shown in Figures 1, 2, 3, and 4, respectively. A suggested overlay structure for loading PR2D is shown in Figure 5.

Listed in Figure 6 is a job control setup deck to compile and load the PR2D program with the MSFC Sigma V CP-V operating system. Compiling and loading require approximately 45 min of CPU time, with load time being insignificant.

A listing of PR2D related files and PR2D object modules created by the FORTRAN compiler and their sizes in granules (1 granule equals five hundred and twelve 32-bit words) are shown in Figure 7.

The created load module (LMPR2DC) requires 146 granules of disc space and approximately sixty four thousand 32-bit words of memory to run. During a normal computer run, PR2D generates data files along with the artwork output file and requires approximately 1000 granules of temporary disc space for a  $200 \times 200$  mil chip. The temporary disc storage requirements are a function of the design complexity. Execution time can vary from 5 min to 1 h of CPU time determined by the design complexity.

#### III. EXECUTING PR2D

The input data to PR2D consists of a number of program control parameters, logic pattern assignments, and the interconnecting nets. The program reads the input data, searches a data file (pin data file) for data on

each pattern, generates a placement of the patterns (PLACE), interconnects the patterns (ROUTE), and outputs the artwork for the layout (ARTWORK). Two examples of layouts that have been created with PR2D are included in this material. The first is a 4-bit adder done in silicon on sapphire and the material includes the partitioned logic diagram (Fig. 8), a complete PR2D computer run printout, and the resulting unmodified chip layout (Fig. 9). The second example is a programmable timing circuit done in bulk silicon and the material includes the partitioned logic diagram (Fig. 10), the job control and input data, and the resulting unmodified chip layout (Fig. 11). Table 1 lists the effects on the layout of the C015 Programmable Timer if the number of logic cell rows are varied by PR2D.

With the MSFC CP-V operating system, PR2D control and input data are created with an on-line editor and batched into the job stream.

By way of the PR2D mode control parameters, functions of the program (place, route, etc.) can be executed in steps for flexibility and reduced computer run times. Due to interaction between the layout control parameters, it is best to start with a working set of parameters (from the included examples) and carefully make variations on them. Their impact upon the layout should be observed by plotting. The included PR2D users guide and control parameter definition list describes the format and effects of each user option. Combinations can be found that achieve a desired layout.

The PR2D program outputs a map or printer plot that can be assembled into the final chip layout. All pertinent data with regard to the layout are shown on the map, and a printout of the chip statistics is output.

It has been our experience that modifications are required on the final PR2D layout. These modifications may be as simple as correcting misplaced test transistors, border modifications, or labeling. However, they have included uncompleted ground bridges to the cells and missing wiring segments. It is recommended that the layouts be carefully studied and each net manually checked against the plotted design layout. At MSFC these modifications are currently done with interactive graphics. However, they were done before by manually editing the PR2D artwork output data.

The EXEC (first routine of the source) contains a well commented section describing the functions of the mode control and layout control parameters, and has been included in this material. It is suggested that this be studied

carefully and used in conjunction with the simplified users guide when using the PR2D automatic layout program.

The PR2D program has been used to create numerous random logic arrays in different technologies at NASA/MSFC. While the program is large and complex, it is practical to implement and to learn how to use. It can produce quick turnaround layouts in mature technologies with acceptable device densities for many applications.

## IV. SIMPLIFIED USERS GUIDE FOR PR2D PROGRAM (INPUT DATA)

The following paragraphs list various input data and control cards for the PR2D program. (Section IV. H. lists PR2D program format modifications.)

### A. Mode Card (412, 1614) Format

The mode card is used to control execution of program functions, control, identification, and debug printing.

- 1) Mode (1), Starting Function
  - = 0.1 start with Input function
  - = 2 start with Place function
  - = 3 start with Route function
  - = 4 start with Art function
  - = 5 start with Manmod function
  - = 6 error condition
- 2) Mode (2), Sub-Restart (within Function)

Start with parameter. Can only be used when Mode (1) = Mode (3); primarily used to debug program.

- = 0.1 start with first sub-function
- = 2 start with second sub-function
- = 3 start with third sub-function
- = 4 start with fourth sub-function

- 3) Mode (3), Ending Function
  - = 1 end after Input function is complete
  - = 2 end after Place function is complete
  - = 3 end after Route function is complete
  - = 4,0 end after Art function is complete
  - = 5 error condition
- 4) Mode (4), Sub-Restart (within Function) Stop After Parameter

  Can only be used when Mode (1) = Mode (3).
  - = 1 stop after first sub-function
  - = 2 stop after second sub-function
  - = 3 stop after third sub-function
  - = 0,4 stop after fourth sub-function
- 5) Mode (5), Chip Identification Number

Must be > 0 and < 1000.

- 6) Mode (6), Technology Switch
  - = 1 is for aluminum gate, bonded wire
  - = 2 is for aluminum gate, beam lead
  - = 3 is for silicon gate, bonded wire
  - = 4 is for silicon gate, beam lead
  - = 5 silicon on sapphire (SOS), bonded wire
- 7) Mode (7), Pin Data File Control
  - = 0,1 pin data are from tape
  - = 2 pin data are from cards and tape
  - = 3 pin data are from cards
- 8) Mode (8), Pin Data Validation Keys = n1, n2, n3, n4

n1n2 = revision level

n3n4 = engineering level

9) Mode (9), Chip Design Type
Internally set to 1

- 10) Mode (10), User Specification of Number of Device Cell Rows
  - = 0 number of rows determined by linear mils of all active cells= n user specification of number of row

1 < n < 13

- 11) Mode (11), User Specification of Number of Bonding Pads on Each Side of Chip
  - = 0 program determined
  - = n user specification

n must be greater than number calculated by program

- 12) Mode (12), Automatic Change of Specific Constants in Coding
- 13) Mode (15), Debug Control for Common data

Equal to 1 or 2, print common data after Read of common data from tape

Equal to 2 or 3, print common data after Write of common data to tape

- 14) Mode (16), Debug Control for Place Function
- 15) Mode (17), Debug Control for Route Function
- 16) Mode (18), Debug Control for Artwork Function
- 17) Mode (19), Debug Control for Manmod Function
- 18) NEW Column 80 Parameter Patch Control

NEW = 1 - next card patches IP(JX)=JY

- = 2 The IP array is replaced by the next IPX words read in format 20I4 from cards which follow. Elements which are blank or zero on the patch cards will not be changed; however, no further patches are permitted.
- = 3 next card patches LIM(JX)=JY
- = 0 no additional patches will be made.

Cards read in modes 1 and 3 will contain the following data in Format 3I4: JX, JY, NEW. The value of NEW read in this field determines the mode in which the next card is to be read.

## B. Pin Data File (Cards or Tape), Header Data (10A4, 414)

If data are on tape, file cannot be opened unless KEY1, KEY2, and KEY3 are valid. If data are on cards and on tape, new header card will be copied to the new pin data tape (if generated). If header card data are blank, the header data from the old tape will be copied to the new pin data tape. This also applied to the revision date described in B1.

The program will automatically sort pin data by cell pattern number and by pin number within each cell type. If card data and tape data have the same cell pattern number, the program will automatically replace the new card pin data for the old tape pin data.

- 1) Pin Data File Description (10A4) Last 8 characters should be date file was generated in nn/nn/nn format.
  - 2) KEY1 Revision level of data, see Mode (8) data.
  - 3) KEY2 Engineering level of data, see Mode (8) data.
  - 4) KEY3 Technology key for data, see Mode (6) data.
  - 5) NEW The control to generate a new pin data tape.

- a) If equal to zero, new pin data file tape will be generated.
- b) If greater than zero, a new pin data file tape will NOT be generated.

# C. Cell Pattern Header Card and Pin Data Cards (514, 10A4, 14) Format

- 1) The pattern number must be entered on all cards (including header card).
- 2) The pin number must be zero for header card. The number of pins on each cell is not limited (can be 1 to N). Furthermore, not all pins need to exist and the pins need not be in numerical order.
- 3) The pin reassignment flag must be zero on header card and on last card of each cell pattern. This last card is used to specify the X/Y limits of that cell.
- 4) The pin X-coordinate must be zero on the header card. Enter the X-distance of the center of the pin from the origin of the cell in tenths of mils.
- 5) The pin capacitance must be zero on the header card on reference pin Y-coordinate of cell from cell origin (distance of ground center to cell origin).
- 6) The cell description data are entered on header card and optionally on pin data cards.
- 7) The technology code [same value as Mode (6)] must be entered on each and every card.

### D. Stop Control Card

The stop control card following last pin data card is used to end reading of pin data file from cards. End of file will terminate reading pin data file from tape. The stop control card can be:

- 1) A blank card
- 2) "STOP" entered in columns 25-28 of stop control card
- 3) "ENDb" or "bEND" entered in columns 25-28 of stop control card.

### E. Element to Pattern Assignment (1614, 14X, 12) Format

Element number-pattern-position-row assignment are entered four to a data card. Elements need not be in numerical order or in sequence. That is, the chip may contain N elements and be assigned any unique element number between a and b, where  $1 \le a \le N \le b \le 400$ \*.

\*Because the program automatically generates 1 or more subelements for large cells, b + N must be less than or equal to 400.

Element data are generated in sets as follows:

- 1) Element number
- 2) Pattern number
- 3) User specification of fixed order of element in a specified row. (For nonbonding pad rows, all elements of that row MUST be assigned, but their order need not be assigned.)
  - 4) Fixed row specification of element; i.e.,

For ROW = -N, element will be in reverse orientation in row N. For ROW = N, element will be in normal orientation in row N. For Row = 0, element is not fixed to any row and will not be assigned to any row which contains any fixed row elements.

Note: If user specifies any nonbonding pad element as fixed in a row, all other elements in that row must also be fixed. Specification of order within a row implies that the row must be specified, but the converse is not true.

The program will terminate reading element/pattern data when an element and corresponding pattern are both zero or when a positive number is entered in columns 79-80 in the last card of these data. Once the element to pattern assignment has been completed, the program will automatically extract those patterns used in the chip design from the pin data file.

### F. Node List

The program will stop reading node data when an end of file is read or if columns 77-80 of the last card has a value greater than zero.

- 1. First Card of Node Card (2014) Format. This card contains the node weighting factor followed by element pin data in pairs (up to column 76). If a node can be completed in one data card, a node continuation card must be used and the pin for the last element must be the first entry on the continuation card. Columns 73-76 of the node card MUST be zero.
- 2. Node Continuation Card (8X, 18I4) Format. This card is used to read the pin number of the last element on the previous data card and additional element-pin data in pairs (columns 9-76). Node continuation cards may continue to be used, as required, by making the pin number of the last element on a data card blank and placing the pin number in the first field of the next continuation data card.

### G. Commod Data Cards (414, 2A4) Format

These data cards are used to modify any data in any array in common. This option can only be exercised (automatically) after common data have been recovered from magnetic tape (READ subroutine).

- 1) LL1 is the i index of the data in the array being modified.
- 2) LL2 is the j index (if required) of the data in the array being modified.
- 3) LL3 is the k index (if required) of the data in the array being modified.
- 4) LL4 is the number assigned to each array in common:

```
LL4 = 1 for CHIP (I)

LL4 = 2 for ICAP (I)

LL4 = 3 for INFO (I, J)

LL4 = 4 for IP (I)

LL4 = 5 for IPIN (I, J)

LL4 = 6 for LCS (i)

LL4 = 7 for LIM (I)

LL4 = 8 for MODE (I)

LL4 = 9 for NODE (I)

LL4 = 10 for JART (I, J)

LL4 = 11 for JFAC (I, J)

LL4 = 12 for MAP (I, J, K)

LL4 = 13 for NOD (I)

LL4 = 14 for NR (I, J)
```

This number is optional. If the array name is entered for ALP variable (see item 7), the program will calculate LL4.

- 5) LL5 is the new data to be stored in common (for numeric data).
- 6) BET is the new data to be stored in common (for alpha data).
- 7) ALP is the first four characters of the name of the array in which data are being changed. This is optional. If the array number is entered for LL4, these data are not required.

### H. PR2D Program Format Modifications

The latest modification of PR2D which included the SOS technology requires some input data modifications. In all cases, the data format changes are minor. If old data are to be used with the new program, only a few minor changes are required. The reason for these data format changes is to confine all data between columns 1 and 72. No data are allowed in columns 73 to 80. The following are the changes:

- 1) MODE Data Card The new format is (412, 1614).
- 2) Parameter Modification Cards (INIT) The effective format is now (II, I3, 2I4). The actual format is still (3I4). See INIT subroutine for details. II and I3 represents V and C in equation IP(IV + C) = New Data. For example, columns 1 to 4 should be:

To Modify	<u>Data</u>	Alternate*
IO + 10	0b10	7b11
I1 + 10	1b <b>10</b>	7111
<b>I</b> 2 + 10	2b10	7151

<sup>\*(</sup>b = space or zero)

That is, the user may enter the IV variable plus the offset constant C to calculate the correct index in the array IP(I), where  $V=0,\,1,\,2,\,3,\,4,\,5,\,$  or 6. If the alternate method is used, the user must calculate the correct index (IV + C) for IP(I) and MUST prevent the program from adding the variable V to that index. To prevent the program from adding V, set V=7.

3) Parameter Modification Cards (COMMOD) — This operates exactly like item 2. The effective format is now (II, I4, ...). For example:

To Modify	Data*	Alternate*		
IO + 10	0bb10	7b11		
*(b = space or	zero)			

- 4) Element to Pattern Assignment The new format is (I6I4, 6X, I2).
- This change has no effect on the element to pattern data. It only effects the skip data/stop control. These data must now be in columns 71 and 72.
- 5) Node List The node list format is now (18I4). Generally, this will require no changes to the node data. The only change is that when there is exactly 9 (17, 25, ...) pins in a node, the pin for that 9th (17th, 25th, ...) element MUST be entered on the next data card. Formally, the program had allowed a nine pin node to be totally contained on one data card. The new program requires the use of two data cards for that nine pin node. Nodes with 2 to 8 pins, 10 to 16 pins, etc. are not effected.
- 6) Restart Mode When PR2D is executed in the restart mode, a chip description data card must follow the mode data card in the RESTART mode.

### I. Execution

The following are the suggested methods for execution of the PR2D program:

- 1) The PR2D program should be executed one function at a time. Without exception, the placement function and route function should NOT be executed in the same computer run. The output of the placement function should be examined. Sometimes interchanging two adjacent bonding pads can improve the final chip design. See design improvement.
- 2) The PR2D program should be executed at least two times using a different placement surface for each run (See Section III). Each resulting chip design should be plotted on a Calcomp plotter.
- 3) The best chip design (smallest area) (Paragraph IV.B) should be used for the final chip design.

### J. Design Improvement

Using the COMMOD method to change data in common, the program can be controlled to generate different placements and make improvements in these placements. Changes to the placement surface will generate these different placements.

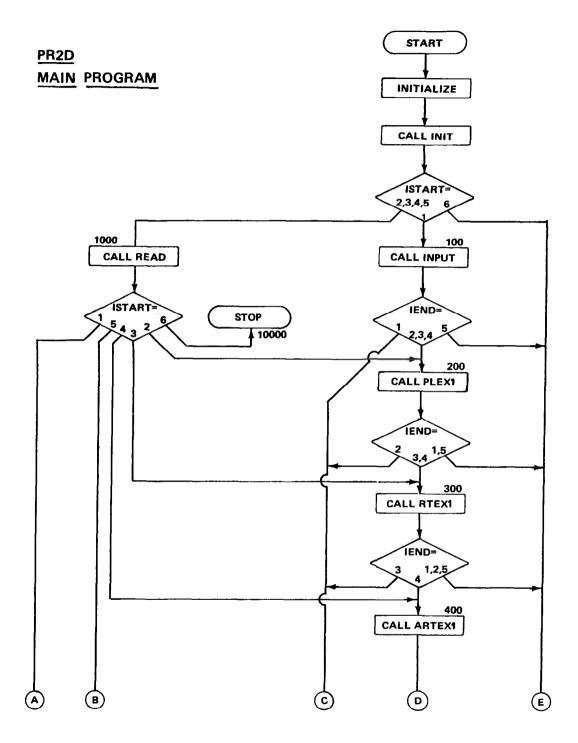


Figure 1. PR2D main program flowchart.

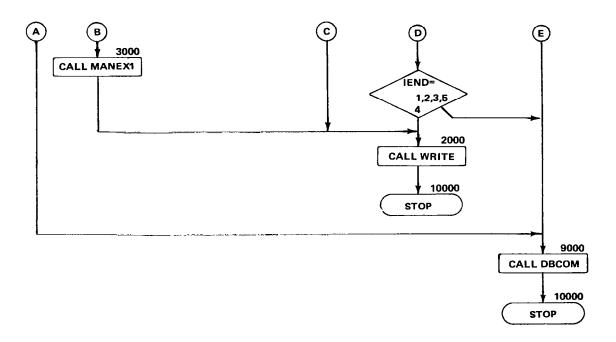


Figure 1. (Concluded).

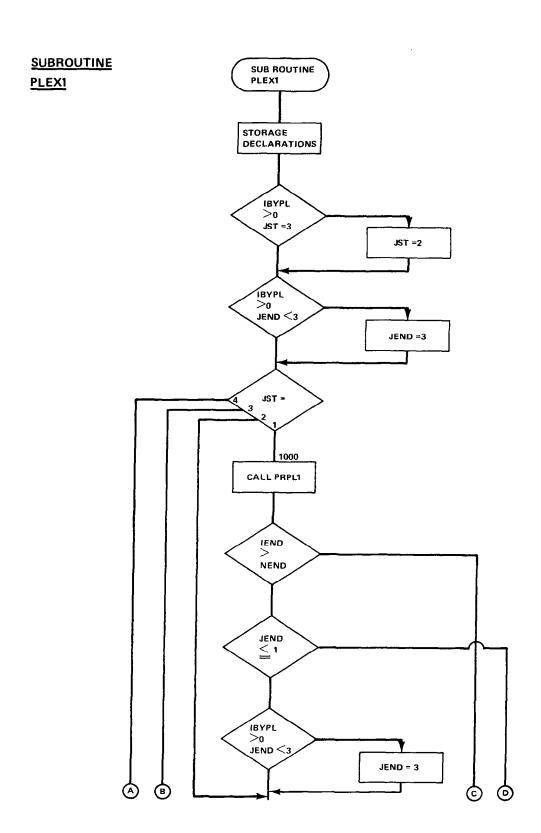


Figure 2. PR2D place executive flowchart.

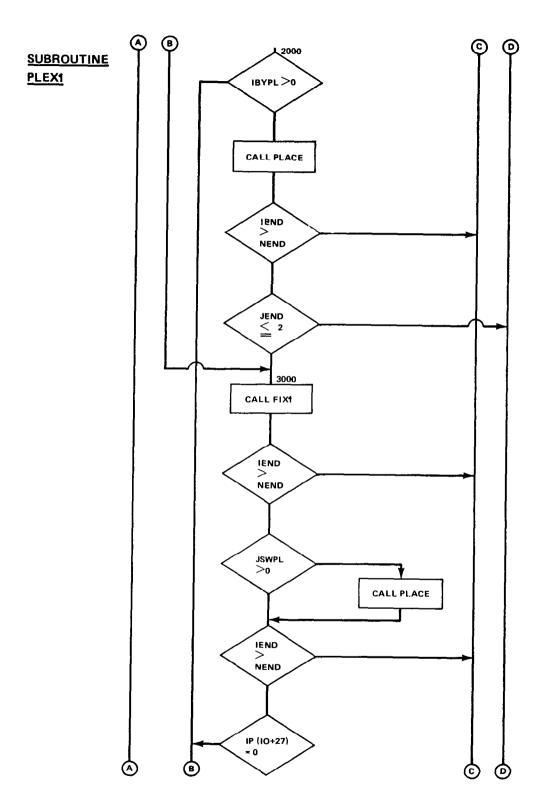


Figure 2. (Continued).

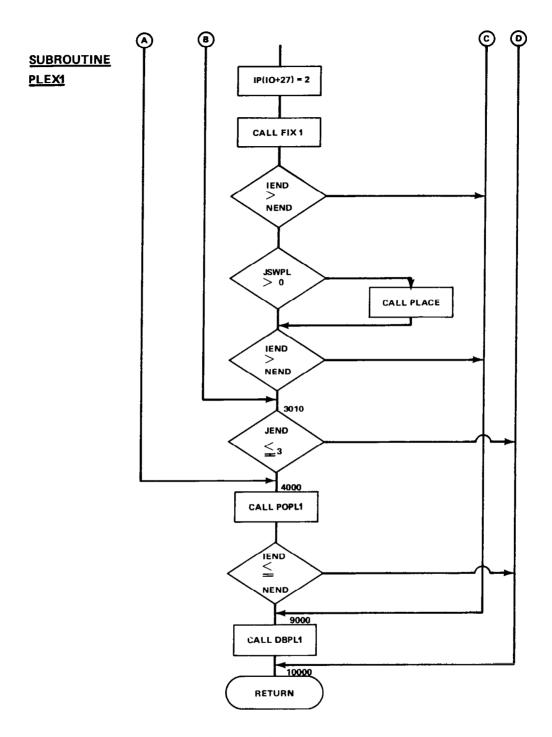


Figure 2. (Concluded).

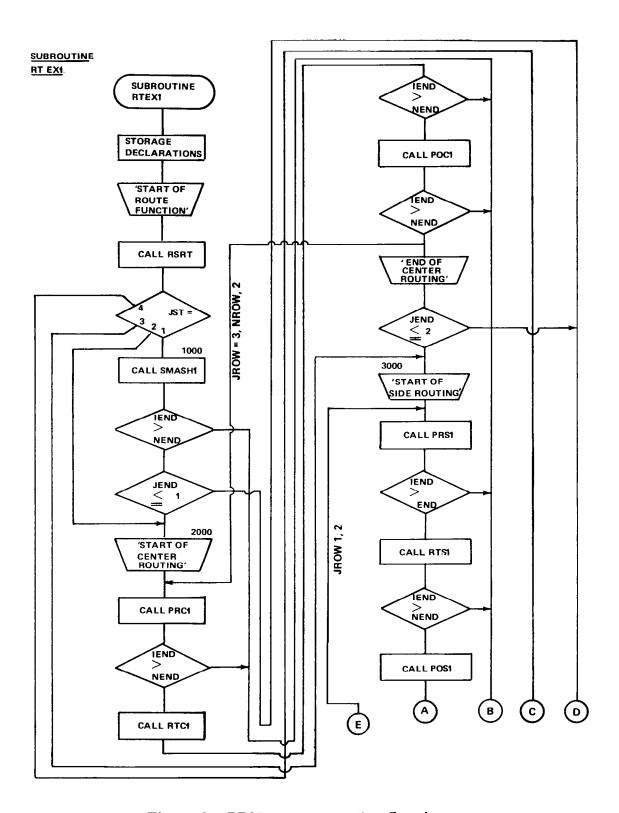


Figure 3. PR2D route executive flowchart.

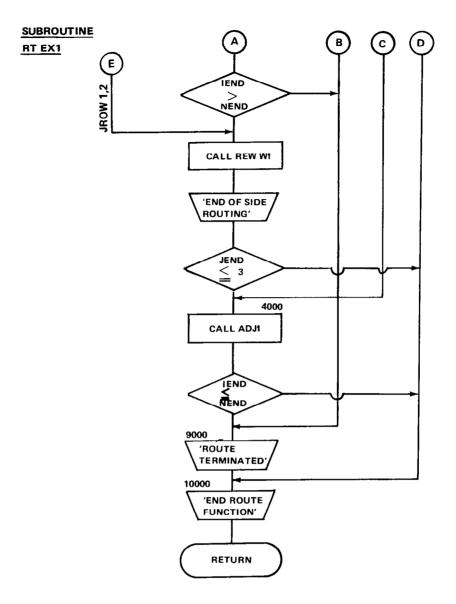


Figure 3. (Concluded).

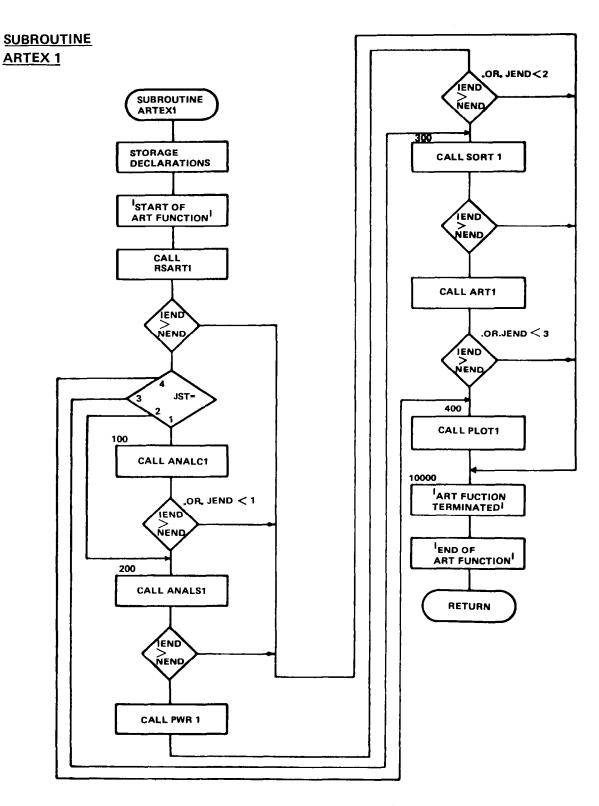


Figure 4. PR2D artwork executive flowchart.

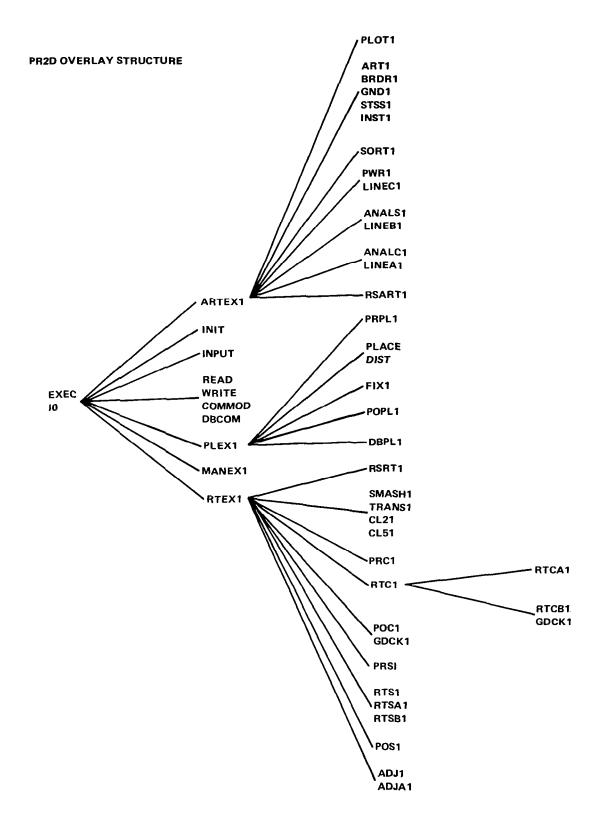


Figure 5. PR2D overlay structure.

```
IJOB PM20,EC45020(0ASI/CAU).1
ILIMIT (CORE,72),(Time,45)
IASSIGN MISI,(DEVICE,97),(SN,PM20),(IN)
IASSIGN MIHO,(FILE,KEC ),(SAVE)
IFONTRAN PS,ND
IASSIGN MIHO,(FILE,INIT ),(SAVE)
IFORTRAN PS,NO
IASSIGN MIHO,(FILE,INPIT ),(SAVE)
IFORTRAN PS,NO
IASSIGN MIHO,(FILE,INPIT ),(SAVE)
IFORTRAN PS,NO
IASSIGN MIHO,(FILE,INPIT ),(SAVE)
                                                                                                                                                                                                                                                                                                                                                                      IFORTRAN PS,30
IASSIGN MIND.(File,ANALSI),(Save)
IFORTHAN PS,MU
IASSIGN MIND.(File,LINEHI),(SAVE)
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IFORTHAN PS, MO
IASSIGN MIND, (FILE, MAITE ), (SAVE)
IASSIGN MIND, (FILE, FOMMOD), (SAVE)
IFORTHAN PS, MO
IASSIGN MIND, (FILE, DECOM ), (SAVE)
IFORTHAN PS, MO
IASSIGN MIND, (FILE, DECOM ), (SAVE)
IFORTHAN PS, MO
IASSIGN MIND, (FILE, DECOM ), (SAVE)
IFORTHAN PS, MO
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    IASSIGN MIHO, (FILF, FLEXI ), (SAVE)
IFONTHAN PS, 80
IFORTHAN P3.HO

IASSIGN MHDO, (FILE, PRPLI), (SAVE)
IFORTRAN P3.HO

IASSIGN MHDO, (FILE, PLACE), (SAVE)
IFORTRAN P3.HO

IASSIGN MHDO, (FILE, D137), (SAVE)
IFORTRAN P3.HO

IASSIGN MHDO, (FILE, FIX1), (SAVE)
IFORTRAN P3.HO

IASSIGN MHDO, (FILE, PDPLI), (SAVE)
IFORTRAN P3.HO

IASSIGN MHDO, (FILE, PDPLI), (SAVE)
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LASSIGN MINU, (FILE, 10), (SAVE)
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IASSIGN M:80.(FILE,08PL1 ),(SAVE)
   IFORTHAN PS,H)
IASSIGN MIBO,(FILE,HTEXT ),(SAVF)
IFORTHAN PS,H)
 IFORTHAN PS, BO
IASSIGN MIHO, (FILE, MSHT1 ), (SAVF)
IASSIGN MIHO, (FILE, SMASH1), (SAVF)
IASSIGN MIHO, (FILE, SMASH1), (SAVF)
IASSIGN MIHO, (FILE, THANS1), (SAVE)
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IASSIGN MIHO, (FILE, LLZ1 ), (SAVF)
IFORTHAN PS, HO
INDIVIDED TO THE PS, HO PS
 IFORTHAN PS,00
IASSIGN "HOU.(FILE,CLS! ),(SAVE)
IFORTHAN PS,00
IASSIGN WHOD,(FILE,GDCK1 ),(SAVE)
IFORTHAN PS,00
IASSIGN WHOU,(FILE,PPC1 ),(SAVE)
IFORTHAN PS,00
    IASSIGN MIHO, (FILE, RTC1 ), (SAVF)
IFORTRAN PS, HU
IASSIGN MIHO, (FILE, RTCAI ), (SAVF)
 | IASSIGN WIND, (FILE, PTCA) ), (SAVE) | IFORTHAN PS, MO | IASSIGN MIND, (FILE, PTCH) ), (SAVE) | IFORTHAN PS, MO | IASSIGN MIND, (FILE, P)C1 ), (SAVE) | IFORTHAN PS, MO | IASSIGN MIND, (FILE, PHS) | IFORTHAN PS, MO | IASSIGN MIND, (FILE, PHS) | IFORTHAN PS, MO | IASSIGN MIND, (FILE, PTS) | IA
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| \Puf,:
| READ-ARITE-COMMOD-05COM,:
                                                                                                                                                                                                                                                                                                                                                                                                                   PLE41-(PRPCL1, PLACE-DIST, F[Y1, POPL1, DBPL1),;
RTE41-(48-71, SYAS-41-TA-XS)-CL21-CL51, PPC1, HTC1-(RTC41, RTC31-GGCK1;
), PJC1-GJCK1, PPS1, PTS1-HTS41-FTS81, PPS1, 40J1-40J41),;
   (ASSIGN MINO, (FILE, MTS) ), ( GAVE)
                                                                                                                                                                                                                                                                                                                                                                                                                     ARTEXI - (*SARTI, ANALCI - LIMEAI, ANALSI - LIMEBI, PARI - LIMECI, SORTI,;
ARTI - dROR) - GNOI - STSSI - (*STI - PLOTI);
   IFOHIKAU PS, mil
    LASSIGN MIND, (FILE, HT : AT ), (54VE)
                                                                                                                                                                                                                                                                                                                                                                                                                     VA.IEXI)
   IFORTHAM PS.DO
                                                                                                                                                                                                                                                                                                                                                                       LEOD
IFORTHAIL PS, DO.

IASSIGN 41HU, (FILE, MISHI ), (NAVE)

IFORTHAN PS, MI

IASSIGN MIHU, (FILE, PUSI ), (SAVE)

IFORTHAIL PS, MI

IASSIGN MIHU, (FILE, AUJI ), (SAVE)

IFORTHAIN PS, MI

IASSIGN MIHU, (FILE, AUJII ), (SAVE)

IFORTHAIN PS, MI

IFORTHAIN PS, MI

IFORTHAIN PS, MI
   LASSIUN MIND, (FILE, ANTEAL), (SAVE)
LEONTHAN PS. HID
 IFORTHAN PS, BO
IASSIGN MIHU, (FILE, HSARTI), (SAVE)
IFORTHAN PS, BO
IASSIGN MIHU, (FILE, AVAICE), (SAVE)
   IFORTHAN MS. HU
IASSIGN MINU. (FILE. LI JEAT). (SAVE)
```

Figure 6. Compile and load job setup.

GRAN	REC	NAME
4	51	ADJA1
4	55	ADJ1
10	145	ANALC1
5	67	ANALS1
1	8	ARTEX1
3 3	38 39	ART1 BRDR1
2	22	CL21
ĩ	10	CL51
3	34	COMMOD
5	63	DBCOM
2	15	DBPL1
2	15	DEBLOCK
3	45	DIST
1	6	EXEC
6	83	FIX1 GDCK1
1 4	7 51	GND1
8	117	INIT
11	167	INPUT
1	5	INST1
3	34	10
1	11	LINEA1
1	11	LINEB1
1	11	LINEC1
146	96	LMPR2DC
10	152	MANEX1
4	56	PLACE
1 1	6 6	PLEX1 PLOT1
23	360	POC1
23 13	202	POPL1
6	93	POS1
3	31	PRC1
6	81	PRPL1
5	70	PRS1
5	139	PR2DCMPLD
1244	30084	PR2DS
4	60	PWR1
2	24	READ
1 1	11 9	RSART1 RSRT1
4	50	RTCA1
7	105	RTCB1
2	17	RTC1
1	12	RTEX1
1	9	RTSA1
4	48	RTSB1
2	28	RTS1
20	325	SMASH1
5	74	SORT1
2 3	15 34	STSS1 TAPES
2	34 57	TEMP
1	57 5	TRANS1
3	31	WRITE
-		<b>-</b>

Figure 7. PR2D source, object modules and load module disc usage.

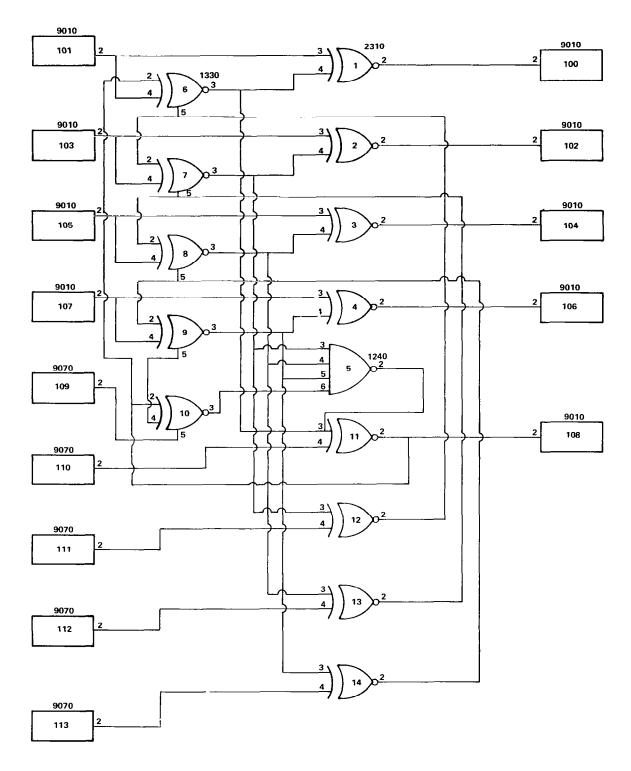
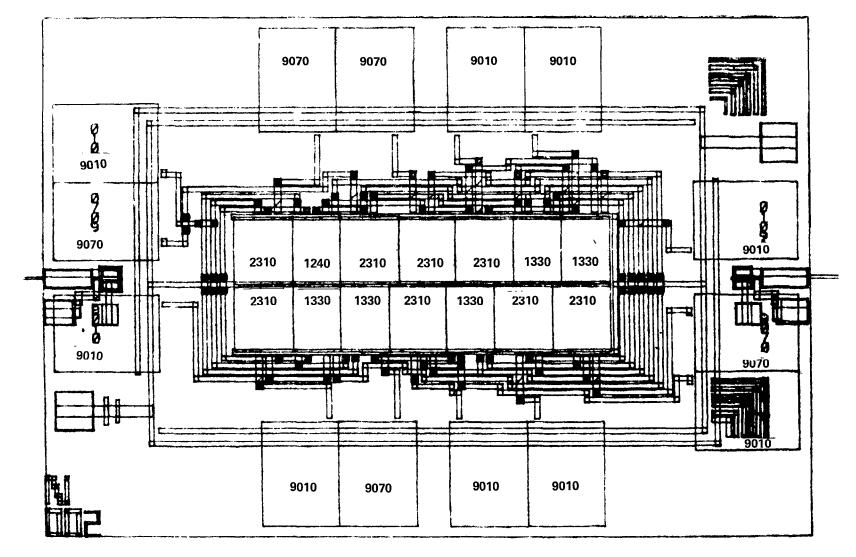


Figure 8. N002 - 4 bit adder partitioned logic diagram.



### METAL, POLY AND DUMMY CELLS

Figure 9. N002 - PR2D unmodified layout.

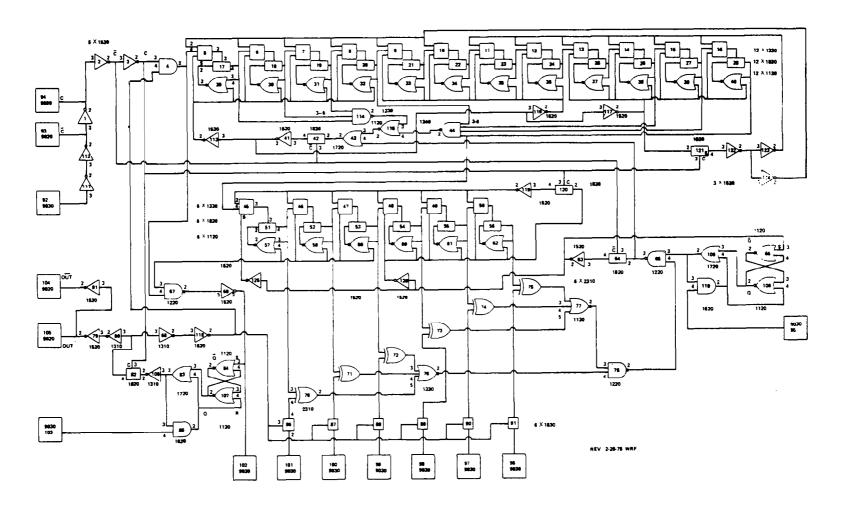


Figure 10. C015 — programmable timer partitioned logic diagram.

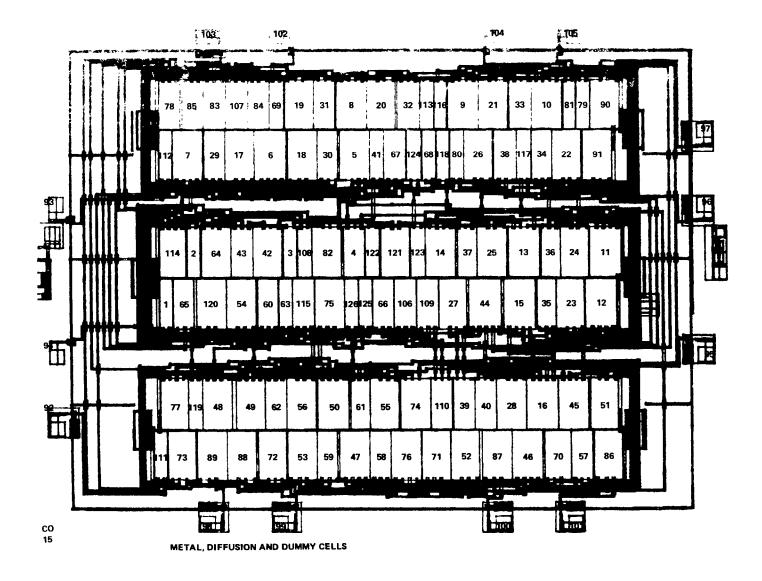


Figure 11. C015 - PR2D unmodified layout.

### TABLE 1. EFFECTS OF VARYING THE NUMBER OF LOGIC CELL ROWS ON THE PR2D LAYOUT OF THE C015 PROGRAMMABLE TIMER

Number of Logic Cell Rows	Step and Repeat (X × Y) Dimensions (mils)	Chip Area (mils²)	Longest Cell Row (mils)	Total Cell Row Length (mils)	Cell Area (%)	Bonding Pad Area (%)	Wiring Area (%)	Wiring Effect (%)	Inches of Metal	Inches of Tunnel	Number of Tunnel Ends
5	237 × 156	36 972.0	156.3	761.6	28.8	41.7	29.5	29.5	6.75	0.88	313
PR2D Program Choice	\ 			'							
6	213 × 169	35 997.0	129.4	761.6	29,6	41.5	28.9	59.8	6.46	0.90	379
7	201 × 193	38 793.0	110.7	761.6	27.5	39.9	32.7	51.3	6.96	0.86	371
8	187 × 211	39 457.0	97.9	761.6	27.0	39.6	33.3	48.7	6.88	0.83	421

# V. PR2D MODE AND LAYOUT CONTROL PARAMETER DEFINITIONS

```
000 0100
                                                                         000-0300
000 0500
C
          AUTHOR
                       : RICHARD NOTO
                                                                         000 0600
C
                                                                         000 0700
          DATE WRITTEN: 1972
                                                                         000 0800
C
                                                                         000 0900
          COMPANY
                       : RCA CORP.
                                                                         000 1000
000 1100
                       : MAIL STOP 10-701
Č
          ADDRESS
                         FRONT & CUOPER STS.
                                                                    000 1300
                                                                         000 1200
                         CAMDEN, N. J., 08102
                                                                         000 1400
C
          PHONE NUMBER: (609) 963-8000
                                                                         000 1500
                                                                         000 1600
000 1700
         EXTENSION : PC6755
C
000 1900
      THE PURPOSE OF THE PR2D PROGRAM IS TO ASSIST IN THE DESIGN OF LSI 000 2000 ARRAYS. BASIC DESIGN DATA (ASSIGNMENT OF ELEMENT NUMBERS TO 000 2100
C
      PATTERNS AND NODE CONNECTIVITY DATA) ARE TRANFORMED TO ARTWORK INSTRUCTIONS. THESE INSTRUCTIONS ARE USED BY THE ARTWRK PROGRAM
                                                                         000 2200
C
                                                                       C
      TO GENERATE ARTWORK COMMANDS USED FOR PLOTTING FINAL ARTWORK
                                                                         000 2400
C
                                                                         000 2500
      MASKS.
C
                                                                         000 2600
      THIS PROGRAM PROVIDES TWO-DIMENSIONAL PLACEMENT OF CELLS ON THE
                                                                         000 2700
C
      THIS PROGRAM PROVIDES INCOMENSIONAL FLOOR OF CONNECTIONS
PLACEMENT SURFACE AND A TWO-DIMENSIONAL ROUTING OF CONNECTIONS
                                                                         000 2800
C
                                                                         000 2900
  AMONG THESE CELLS. EACH SUBROUTINE IS MODULAR AND CAN BE
C
      REPLACED OR MODIFIED.
C
                                                                         000 3000
                                                                         000 3100
000 3300
C
      THE PURPOSE OF THE EXEC PROGRAM IS TO CONTROL THE SEQUENCE OF
                                                                         000 3400
С
                                                                        000 3500
000 3600
      EXECUTION OF THE PR2D PROGRAM. THE EXEC PROGRAM ITSELF HAS BEEN
T
      KEPT TO MINIMUM SIZE AND ALL FUNCTIONS ARE OVERLAID AT THE END
--€
      OF EXEC SO THAT THE STORAGE REQUIREMENTS FOR THE PR2D PROGRAM
                                                                         000 3700
C
      WILL BE MINIMUM. THE EXEC PROGRAM IS CONTROLLED BY THE MODE
C
                                                                         000 3800
      PARAMETERS. THESE PARAMETERS SPECIFY THE START AND END OF
                                                                         000 3900
C
      EXECUTION OF THE PR2D PROGRAM. THIS FEATURE IS CALLED "RESTART".
                                                                         000 4000
C
      THIS FEATURE ALLOWS THE USER TO INTERUPT THE OPERATION OF THE
                                                                         000 4100
C
      PR2D PROGRAM AND, IF DESIRED, MAKE CHANGES TO THE DATA STORED
                                                                         000 4200
C
      IN COMMON. ALL INTER-FUNCTION (PL,RT,ETC.) DATA ARE TRANSFERRED
C
                                                                         000 4300
      THROUGH COMMON OR THROUGH MAGNETIC TAPE. THOSE ARRAYS THAT ARE NOT IN COMMON ARE ONLY USED WITHIN THAT PARTICULAR FUNCTION.
                                                                         000 4400
C
C
                                                                        000 4500
      IF NON-COMMON DATA MUST BE TRANSFERRED, IT WILL EITHER BE WRITTEN 000 4600
      TEMPORARILY TO COMMON OR TO MAGNETIC TAPE."
                                                                        000 4700
C
                                                                         000 4800
     Citat
                                                                         000 5000
С
      RESTART FEATURE:
                                                                         000 5100
C
                                                                         000 5200
C
        MODE(1) = ISTART IS THE STARTING FUNCTION PARAMETER.
                                                                        000 5300
C
                = 0, 1 START WITH INPUT FUNCTION.
                                                                        000 5400
                       START WITH PLACE FUNCTION.
START WITH ROUTE FUNCTION.
START WITH ART FUNCTION.
                                                                      000 5500
C
                = 2
                                                                        000 5600
C
                = 3
                                                                        000 5700
C
```

```
C
                         START WITH MANMOD FUNCTION.
                 = 5
                                                                              000 5800
                         ERROR CONDITION SWITCH.
                                                                              000 5900
Ē
                 = 6
                                                                              000 6000
        MODE(2) = JST, IS THE SUB-RESTART, WITHIN FUNCTION, START WITH 000 6100
C
                     PARAMETER. SEE EACH FUNCTION EXECUTIVE FOR
C
         (3-4)
                                                                              000 6200
                      DETAILED INFORMATION ON JST.
                                                                              000 6300
                      THIS CONTROL IS ONLY USED FOR DEBUGGING OF THE
Ċ
                                                                              000 6400
                      PROGRAM AND CAN ONLY BE EXERCISED WHEN MODE(1) =
                                                                              000 6500
                     MODE (3).
                                                                              000 6600
C
                                                                              000 6700
        MODE(3) = IEND IS THE ENDING FUNCTION PARAMETER.
                                                                              000 6800
                         END AFTER INPUT FUNCTION IS COMPLETE.
                                                                          000 6900
                 = 1
                                                     IS COMPLETE.
                         END AFTER PLACE FUNCTION
                 = 2
                                                                              000 7000
                 = 3 END AFTER ROUTE FUNCTION IS COMPLETE.
= 4, 0 END AFTER ART FUNCTION IS COMPLETE.
                                                                            000 7100
000 7200
Č
C
                         ERROR CONDITION SWITCH.
                                                                              000 7300
                                                                              000 7400
        MODE(4) = JEND, IS THE SUB-RESTART, WITHIN FUNCTION, STOP
                                                                              000 7500
        (7-8)
                     AFTER PARAMETER. SEE EACH FUNCTION EXECUTIVE FOR
                                                                              000 7600
C
                      DETAILED INFORMATION ON JEND.
                                                                              000 7700
C
                      THIS CONTROL IS ONLY USED FOR DEBUGGING OF THE
                                                                              000 7800
C
                     PROGRAM AND CAN ONLY BE EXERCISED WHEN MODE(1) .=
                                                                              000 7900
                      MODE(3).
                                                                              000 8000
                                                                              000 8100
        MODE(5) = ID IS THE CHIP IDENTIFICATION # (0 < ID < 1000).
                                                                              000 8200
        (9-12) (SEE INPUT, READ & WRITE FUNCTIONS)
C
                                                                  000 8300
000 8400
000 8500
C
C
        MODE(6) = ITECH, IS THE TECHNOLOGY CONTROL SWITCH,
        (13-16) = 0,1 IS FOR METAL GATE BONDED WIRE.
                                                                             000 8600
                                                                            -000 8700
000 8800
C
                         IS FOR BEAM LEAD METAL GATE.
                 = 2
C
                 = 3
                         IS FOR POLY-SILICON.
                         IS FOR BEAM LEAD POLY-SILICON.
C
                 =
                                                                              000 8900
                         IS FOR SOS BONDED WIRE
                                                                              000 9000
C
                 = 5
C
                         IS FOR SOS BEAM LEAD.
                                                                              000 9100
C
                                                                              000 9200
        MODE(7) = PIN DATA FILE SWITCH.
                                                                              000 9300
C
        (17-20) (SEE INPUT FUNCTION).
                                                                              000 9400
                 = 0, 1 PIN DATA ARE FROM TAPE.
= 2 PIN DATA ARE FROM TAPE AND FROM CARDS.
C
                                                                              000 9500
                                                                       000 9600
C
C
                         PIN DATA ARE FROM CARDS.
                                                                              000 9700
C
                                                                              000 9800
C
        MODE(8) = PIN DATA VALIDATION KEYS.
                                                                              000 9900
        (21-24) = N1, N2, N3, N4. N1N2 IS THE REVISION LEVEL OF DATA.
C
                                                                              000 0000
                                    N3N4 IS THE ENGINEERING LEVEL OF DATA.000 0100
C
                                                                              000 0200
        MODE(9) = ITYPE, IS CHIP DESIGN.
                                                                              000 0300
        (25-28) = 0, 1 \text{ IS TYPE 1 DESIGN.}
                                                                              000 0400
                                                                              000 0500
        MODE(10) IS USER SPECIFICATION OF NUMBER OF CELL ROWS.
                                                                              000 0600
         (29-32) = 0 PROGRAM DETERMINES NUMBER OF ROWS.
                                                                              000 0700
                 = N USER SPECIFIES NUMBER OF ROWS, T < N < 21.
                                                                             000 0800
                                                                              000 0900
        MODE(11) IS USER SPECIFICATION OF NUMBER OF BONDING PADS
                                                                              000 1000
                 ON EACH EDGE OF CHIP. MUST BE > # CALCULATED BY
                                                                              000 1100
                  PROGRAM
                                                                              000 1200
                                                                              -000 1300
        MODE(12) IS SPECIAL CHIP DESIGN CONTROL SWITCH.
                                                                              000 T400
                  = 1 IS PARAMETER MODIFICATION OPTION NO. 1.
= 2 IS PARAMETER MODIFICATION OPTION NO. 2.
= 3 IS PARAMETER MODIFICATION OPTION NO. 3.
= 4 IS PARAMETER MODIFICATION OPTION NO. 4.
        (37 - 40)
                                                                              000 1500
                                                                         000 1600
                                                                              000 1700
                                                                  000 1800
```

```
≈ 5 IS PARAMETER MODIFICATION OPTION NO. 5.
                                                                               000 1900
                       IS PARAMETER MODIFICATION OPTION NO. 6.
                                                                               000 2000
                       IS PARAMETER MODIFICATION OPTION NO.
                                                                               000 2100
                        IS PARAMETER MODIFICATION OPTION NO. 8.
                     8
                                                                               000 2200
                   = 9 IS PARAMETER MODIFICATION OPTION NO.
                                                                               000 2300
                                                                               000 2400
         MODE(15) IS A DEBUG CONTROL FOR COMMON DATA PRINT IN READ
                                                                               000 2500
         (49 - 52)
                  AND WRITE.
                                                                               000 2600
                  = 1, 2 PRINT COMMON DATA IN READ FUNCTION.
                                                                               000 2700
                  = 2, 3 PRINT COMMON DATA IN WRITE FUNCTION.
                                                                               000 2800
                  = IDBCOM.
                                                                               000 2900
                                                                               000 3000
         MODE(16) ≈ IDBPL, IS DEBUG CONTROL FOR PLACE FUNCTION.
                                                                               000 3100
         (53-56)
                                                                               000 3200
                                                                               000 3300
         MODE(17) ≈ IDBRT, IS DEBUG CONTROL FOR ROUTE FUNCTION.
                                                                               000 3400
                                                                               000 3500
         (57-60)
                                                                               000 3600
000 3700
         MODE(18) = IDBART, IS DEBUG CONTROL FOR ARTWORK FUNCTION.
                                                                               000 3800
         (61 - 64)
                                                                               000 3900
         MODE(19) = IDBMAN, IS DEBUG CONTROL FOR MANMOD FUNCTION.
                                                                               000 4000
                                                                               000 4100
                                                                               000 4200
                                                                              ±000 4300
                                                                               000 4400
                                                                               000 4500
                                                                               000 4600
                                                                               000 4700
        * * *
                                                                               000 4800
                                                                               000 4900
       FOR NON-TWO BYTE INTEGER MACHINES, PULL ALL
                                                                               000 5000
            "IMPLICIT INTEGER *2(I-N)" CARDS IN ALL SUBROUTINES.
                                                                               000 5100
                                                                               000 5200
                      FUNCTION
                                 EQUIVALENCE:
                                                  FUNCTION
                                                                  COMMONE
       ARRAY:
                                                                               000 5300
           RELATED
                      USED:
                                                 USED:
                                                                               000 5400
           ARRAY:
                                                                               000 5500
                                                                               000 5600
                                                 NA
                                                                  NO
                                                                               000 5700
                      INPUT
                                 NONE
       AL(JX)
           NONE
                                                                               000 5800
       AL(I)
                      COMMOD
                                 NONE
                                                 NA
                                                                  NO
                                                                               000 5900
           NONE
                                                                               000 6000
                                 NONE
                                                 NA
                                                                  NO
                                                                               000 6100
       AL(I,J)
                      PLOT1
           NONE
                                                                               000 6200
                                                                  NO
                                                                               000 6300
       BETA(JX)
                      INPUT
                                 NONE
                                                 NA
           NONE
                                                                               000 6400
                      POPL1
                                                 NΔ
                                                                  NO
                                                                               000 6500
       BL(I)
                                 NONE
           NONE
                                                                               000 6600
                                                                  NO.
       BL(I)
                      PLOT1
                                 NONE
                                                 NA
                                                                               000 6700
                                                                               000 6800
           NONE
       CHIP(I)
                                 NONE
                                                 NA
                                                                  YES
                                                                               000 6900
                      ALL
                                                                               000 7000
           NONE
                      POPL1
                                 NONE
                                                 NA
                                                                  NO
                                                                               000 7100
       CL(I)
                                                                               000 7200
           NONE
                                                 NA
                                                                  NO
                                                                               000 7300
       DES(I,J)
                      INPUT
                                 NONE
           JPTN(I,J)
                      INPUT
                                 NONE
                                                 NA
                                                                  NO
                                                                               000 7400
                                                 NA
                                                                  ND
                                                                               000 7500
                      PLOT1
                                 NONE
       DL(I)
           NONE
                                                                               000 7600
                                                                  YES
       ICAP(I)
                      ALL
                                 NONE
                                                 NA
                                                                               000
                                                                                   7700
                                                 NA
           NETOT(I)
                      PLACE
                                 NONE
                                                                  ND
                                                                               000 7800
                                                                 NO
           NET(I,J)
                      INPUT
                                 NONE
                                                 NΔ
                                                                              000 7900
```

	MAP(I,J,K)  JP  KP  MODE(I)  LODE(I)	PLACE PLACE PLACE ALL READ	LCS(I) LCS LCS NONE NONE		NONE NONE NONE NA NA			YES YES YES YES		000	4100 4200 4300 4400 4500
	TAPE USAGE IN SUBROUTINE:								· ·· -	000	4600
	- -	N N O T T O O O 1 1 1 0 5	N N O D T T D O 2 2 0 5	N O T O 3	N O T O 3	N 0 T 0 4 0	N 0 T 0 4	N N N O C C S S S S S S S S S S S S S S S S S	)   	N 000 0 000 T 000 0 000 6 000	5100 5200 5300 5400
	EXEC										5600 5700 5800
	INIT									000	5900
•	INPUT			*1			*1	<b>*</b> 2			6100
	READ					x			-		6200 6300
	WRITE				x		-			000	6400 6500
<u> </u>					^					000	6600
3	COMMOD										6700 6800
-	DBCOM								•	000	6900 T
	PLEX1									000	7100
	RSPL1				-						7200
2	PRPL1						-				7400 7500
										000	7600
	PLACE										7700 7800
-	DIST									000	7900
***	FIXI										8000 8100
. <u>:</u>	POPL1										8200 8300
:										000	8400
-	DBPL1										8600
•	RTEX1					-				000	8700
- <del></del> -	RSRT1	x	X						. >	000	8800 8900
	SMASHI		-						<b>v</b>		9000
										000	9200
	TRANS1							<del>-</del>			9300 ° 9400
	CL21		•			•				000	9500
-	TE51		-		-						9600 9700
	PRC1									000	9800 9900
;							_			000	0000
-;	RTC1									000	0100

RTCA1										000	030
RTCB1										000	
										000	060
GDCK1										0 <u>0</u> 0	
P0¢1	X		X							000	
			u							000	100
PRS1			X							000	
RTS1										000	
RTSA1								•		000	
										000	
RTSB1										000 000	
POS1	X									000	190
ADJ1	X 1	X 1								000 000	
	^1	^1				•				7000	
ADJA1										000	
ARTEX1										000	
										000	260
RSART1		X		Х			• •			$\frac{0.00}{0.00}$	
ANALC1		X								000	
				v.						000	
LINEA1				X						000	
ANALS1		×				_				000	33
LINEB1				x						000	
LINCOI				^						000	
PWR1										000	
LINEC1				х						000	
						•				000	40
SORT1				X	•					000 000	
ART1									X 1	*2000	
55554										000	_
BRDR1										$-\frac{000}{000}$	
GND1										000	47
STSS1									×1	000 0005*	
0,001										000	50
INST1	-								X1	_ <del>*2000</del>	
PLOT1		X								000	
							•	-		000	
MANEX 1										0 0 0 0 0 0	
* IF REQUIRED										000	57
X TAPE IS USE	D.				-					000	
****										000 000***	

```
000 6300
                                                                                       000 6400
                                                                                       000 6500
                                                                                       000 6600
000 6700
  ARRAYS IN COMMON:
                                                                                       000 6800
  IMPLICIT INTEGER
                          (I-N)
                                                                                       000 6900
                                       IPRT, JDATA, JPDF,
  INTEGER *4 ICARD, ICORE,
                                                                   KART
                                                                                       000 7000
                                                                                       000 7100
  DIMENSION
                          CHIP(13),
                                             ICAP(400).
                                                                 INFO(500,7),
                                                                                       000 7200
                          IPIN(200,3),
                                                                 LCS1(12000),
       IP(1000),
                                             LCS(12000),
                                                                                       000 7300
                                                                                           7400
       LIM(300).
                          MODE (19).
                                             NODE (4000).
                                                                 NR(1000,7)
                                                                                       000
2
                                                                                       000
                                                                                           7500
                                                         JPDF,
                                    IPRT,
                                                                  KART
                                                                                      000 7600
 COMMON
                 ICARD,
                           ICORE,
                                               JDATA,
                                                                 INFO,
 COMMON
                          CHIP,
                                                                                       000 7700
                                             ICAP,
                          IPIN,
                                                                 LCS1,
                                                                                      000 7800
000 7900
       IP.
                                             LCS,
       LIM,
                                             NODE .
                                                                 NR
                          MODE,
                                                                                      000 8000
 COMMON EQUIVALENCE:
                                                                                      000 8100
                                                                                       000 8200
 EQUIVALENCE
                                                                                      000 8300
                                                          (IEND , MODE( 3)),
(ITECH , MODE( 6)),
                                         ,MODE( 2)),
       (ISTART, MODE( 1)),
                                 (JST
                                                                                      000 B400
       (JEND , MODE ( 4)),
                                         , MODE ( 5)),
                                                                                      000 8500
2
                                 (ID
                                                          (ITYPE , MODE( 9)),
(IDBPL , MODE(16)),
3
                                                                                      000 8600
4
                                 (IDBCOM, MODE (15)),
                                                                                      000 8700
                                                          (IDBMAN, MODE(19))
5
       (IDBRT , MODE(17)),
                                 (IDBART, MODE(18)),
                                                                                      000 8800
 EQUIVALENCE
                                                                                      000 8900
       (IBYPL ,LIM(
                        1)),
                                 (ICAPX ,LIM(
                                                                                      000 9000
1
                                                          (IERR ,LIM( 6)),
(IGDD ,LIM( 9)),
(INFOX ,LIM( 12)),
                                                                                      000 9100
               ,LIM(
                                 (IDY
                                        ,LIM(
2
       (IDX
                        4)),
                                                  5)),
       (IGD
               ,LIMC
                        7)),
                                 (IGDCK ,LIM(
                                                  8)),
                                                                                      000 9200
000 9300
                                 (IGD2 ,LIM( 11)),
               ,LIM( 10)),
       (IGD1
                                 (INFOY1, LIM( 14)),
                                                          (INFOY2, LIM( 15)),
       (INFOY ,LIM( 13)),
                                                                                      000 9400
5
                                                          (INFOY5, LIM( 18)),
(IPAD , LIM( 21)),
(IPINY , LIM( 24)),
       (INFOY3; LIM( 16)),
                                 (INFOY4, LIM("17)),
6
                                                                                      000 9500
       (INFOY6,LIM( 19)),
(IPAGE ,LIM( 22)),
                                (INFOY7, LIM( 20)), (IPINX , LIM( 23)),
7
                                                                                      000 9600
                                                                                      000 9700
8
       (IPINYC, LIM( 25)),
                                (IPINYR, LIM( 26)),
                                                          (IPINYX, LIM( 27)),
                                                                                      000 9800
                                                          (IPX
                                                                                      000 9900
A
       (IPINYY, LIM( 28)),
                                                                   ,LIMC 30)),
      (IROW ,LIM( 31)),
(IY ,LIM( 34)),
(I1 ,LIM( 37)),
                                         ,LIM( 32)),
,LIM( 35)),
,LIM( 38)),
                                                          (ISW2
В
                                                                   ,LIM( 33)),
                                (ISW1
                                                                                      000 0000
C
                                (IZX
                                                          CIO
                                                                   ,LIMC 3611,
                                                                                      000 0100
                                                                   ,LIM( 39)),
                                                                                      000 0200
                                                          (13
D
                                (12
               ,LIM( 40)),
                                                                   ,LIM( 42))
Ε
      (I4
                                (15
                                         ,LIM( 41)),
                                                          (16
 EQUIVALENCE
                                                                                      000 0400
                                (JARTX ,LIM( 50)),
(JARTY2,LIM( 53)),
(JARTY5,LIM( 56)),
               ,LIM( 49)),
                                                                                      000 0500
      (JA
                                                          (JARTY , LIM( 51)),
1
       (JARTY1, LIM( 52)),
                                                          (JARTY3, LIM( 54)),
                                                                                      000 0600
      (JARTY4, LIM( 55)),
                                                          (JARTY6, LIM( 57)),
                                                                                      000 0700
      (JBAD
               ,LIM( 58)),
                                                          (JDEBUG, LIM( 60)),
                                                                                      000 0800
5
      (JEL
               ,LIM( 61)),
                                                                                      000 0900
                                (JELR
                                         ,LIM( 62)),
                                         ,LIM( 65)),
,LIM( 68)),
                                (JL
                                                          (JN
                                                                   ,LIM( 66)),
                                                                                      000 1000
6
                                                                  ,LIM( 69)),
,LIM( 72)),
                                (JNTG
7
      (JNGG
               ,LIM( 67)),
                                                          TJO
                                                                                      000 1100
              ,LIM( 70)),
,LIM( 73)),
                                         ,LIM( 71)),
,LIM( 74)),
                                (JPY
                                                                                      000 1200
8
      (JPX
                                                          (JROW
                                                                   ,LIMC 75)),
      (JRSP
ō
                                (JSL
                                                          CJSWC
                                                                                      000 1300
      (JSWPL ,LIM( 76)),
(JSW3 ,LIM( 79)),
(JTDAMN,LIM( 82)),
                                         ,LIM( 77)),
,LIM( 80)),
                                                                  ,LIM( 78)),
,LIM( 81)),
                                                                                      000 1400
000 1500
A
                                (JSW1
                                                          (JSW2
В
                                (JSW4
                                                          (JSW5
                                (JTDAMO, LIM( 83)),
                                                                   ,LIM( 84)),
                                                                                      000 1600
                                                          (JTOT
C.
                                                                  LIM( 87))
      (JU
               ,LIM( 85)),
                                (JX
                                         ,LIM( 86)),
                                                          (JY
                                                                                      000 1700
 EQUIVALENCE
                                                                                      000 1800
                                (KBAD ,LIM( 95)),
(KLASSY,LIM( 98)),
                                                         (KNODE , LIM( 96)),
                                                                                      000 1900
      (KLASSX, LIM( 97)),
2
              ,LIM(100)),
                                                                                      000 2100
      (KNT
                                (KODE ,LIM(101));
                                                         (KODEC ,LIM(102)),
                                (KODEM ,LIM(104)),
      (KODEG ,LIM(103)),
                                                          (KODET ,LIM(105)),
4
                                                                                      000 2200
      (KODEV ,LIM(106)).
                                (KOUNT ,LIM(107)),
                                                         (KPAD ,LIM(108)),
                                                                                     000 2300
```

```
(KPADN ,LIM(109)),
                            (KPAD1 ,LIM(110)),
                                                  (KPAD2 ,LIM(111)),
                                                                          000 2400
      (KPAD3 ,LIM(112)),
                            (KPMX
                                  .LIM(113)),
                                                  (KPMY
                                                         ,LIM(114)),
                                                                          000 2500
                                                  (KPY
      (KFX
                            (KPX1
8
             , [IM(115)),
                                   ,LIM(116));
                                                          ,LIM(1175),
                                                                          000 5600
      (KROW
             ,LIM(118)),
                                                  (KSXL
9
                            (KSXE
                                    ,LIM(119)),
                                                          ,LIM(120))
                                                                          000 2700
 EQUIVALENCE
                                                                          000 2800
      (KSXR
             ,LIM(121)),
                                                          ,LIM(123)),
                                                                          000 2900
1
                            (KSXT
                                    ,LIM(122)),
                                                  (KSX0
5
      (KŠYB
             ,LIM(124)),
                                    ,LIM(125)),
                                                  (KŤOŤ
                                                          ,LIM(126)),
                                                                          000 3000
                            (KSYT
      (KXLL
             ,LIM(127)),
                            CKXLR
                                                  (KXUL
                                                          ,LIM(129)),
                                    ,LIM(128)),
                                                                          000 3100
7
      CKXUR
             ,LIM(130)),
                            (KXZ
                                    ,LIM(131)),
                                                  (KXI
                                                          ,LIM(132)),
                                                                          000
                                                                              3500
5
             ,LIM(133)),
      (KXS
                            (KX3
                                    ,LIM(134)),
                                                  (KYE
                                                          ,LIM(135)),
                                                                          000 3300
             ,LIM(136)),
                                    ,LIM(137)),
                                                          ,LIM(138)),
6
      (KYR
                            (KYT
                                                  (KYO
                                                                          000 3400
             ,LIM(139)),
                                    ,LIM(140)),
      (KY1
                            (KY2
                                                  (KY3
                                                          ,LIM(141)),
                                                                          000 3500
8
      (K50
             ,LIM(142)),
                            (K100
                                    ,LIM(143)),
                                                  (K150
                                                          ·LIM(144)).
                                                                          000 3600
                                                                              3700
3800
      (K200
             ,LIM(145))
                                                                          000
 EQUIVALENCE
                                                                          0.00
      (LCSX
             ,LIM(154)),
                            (LCXC
                                    ,LIM(155)),
                                                  (LCXM
                                                          ,LIM(156)),
                                                                          000 3900
ž
      (LCXRL
             ,LIM(157)),
                            (LCXRR ,LIM(158)),
                                                  (LCXTL ,LIM(159)),
                                                                          000 4000
      (LCXTR ,LIM(160)),
                            (LCYC
                                   ,LIM(161)),
                                                  (LCYM
                                                          ,LIM(162)),
                                                                          000 4100
3
                            (LCYET ,LIM(164)),
                                                         ,LIM(165)),
      (LCYEB ,LIM(163)),
                                                  (LCYRB
                                                                          000 4200
      (LCYRT ,LIM(166)),
                            (LCYTB ,LIM(167)),
                                                  (LCYTT
                                                          ,LIM(168)),
                                                                          000 4300
6
      (LCYOB ,LIM(169)),
                            (LCYOT ,LIM(170)),
                                                          ,LIM(171)),
                                                  (LE
                                                                          000 4400
7
      (LEFT
             ,LIM(172)),
                            (LIMINT, LIM(173)),
                                                  (LIMX
                                                          ,LIM(174)),
                                                                          000 4500
8
      (LINE
             ,LIM(175)),
                                   ,LIM(176)),
                                                  (LIN3
                                                          ,LIM(17%)),
                            (LIN1
                                                                          000 4600
             ,LIM(178)),
                            (LNADX ,LIM(179)),
                                                  (LROW
                                                          ,LIM(180)),
      (LLE
                                                                          000 4700
      (LSW1
             ,LIM(181)),
Ā
                            (LSW2
                                   ,LIM(182)),
                                                  (LSW3
                                                          ,LIM(183))
                                                                          000 4800
 EQUIVALENCE
                                                                          000 4900
                                    ,LIM(185)),
                                                          ,LIM(186)),
      (LSW4
             ,LIM(184)),
                            (LSW5
                                                  (LSXC
                                                                          000 5000
                            (LSXER ,LIM(188)),
      (LSXEL ,LIM(187)),
                                                  (LSXLL ,LIM(189)),
                                                                          000 5100
2
             ,LIM(190)),
                                                                          000 5200
3
      (LSXLR
                            (LSXM
                                   ,LIM(191)),
                                                  (LSXRL
                                                         ,LIM(192)),
      (LSXRR ,LIM(193)),
                            (LSXTL
                                   ,LIM(194)),
                                                  (LSXTR
                                                          ,LIM(195)),
                                                                          000 5300
5
                                                  (LSYB
      (LSX0L ,LIM(196)),
                            (LSX0R
                                   ,LIM(197)),
                                                          ,LIM(198)),
                                                                          000 5400
                            (LSYM
                                                  (LSYT
      (LSYC
6
             ,LIM(199)),
                                    ,LIM(200)),
                                                          ,LIM(201)),
                                                                          000 5500
             ,LIM(202)),
                                    ,LIM(203)),
                                                          ,LIM(204)),
7
      (LVL
                            (LX
                                                  (LXA
                                                                          000 5600
                            (LX1
8
      (LXSN
             ,LIM(205)),
                                    ,LIM(206)),
                                                  (LX2
                                                          ,LIM(207)),
                                                                          000 5700
      (LX3
             ,((80S)),
                            (LX4
                                    ,LIM(209)),
                                                  (LY
                                                          ,LIM(210)),
                                                                          000 5800
                            (LYSN
      (LYA
             , LIM(211)),
                                    ,LIM(212)),
                                                  (LY1
                                                          ,LIM(213)),
                                                                          000 5900
                                    ,LIM(215)),
                                                          ,LIM(216))
      (LYZ
             ,LIM(214)),
                            (LY3
                                                  (LY4
                                                                          000 6000
 EQUIVALENCE
                                    ,LIM(218)),
                                                          ,LIM(219))
                            (LLX
                                                  (LLX2
                                                                          000 6100
 EQUIVALENCE
                                                                          000 6200
      (MAPX
             ,LIM(223)),
                            (MAPY
                                    , LIM(224)),
                                                  (MAPZ
                                                          ,LIM(225)),
                                                                          000 6300
      CMAX
             ,LIM(226)),
                            (MAXX
                                    ,LIM(227)),
                                                  (MAXX1
                                                          ,LIM(228)),
                                                                          000 6400
2
             ,LIM(229)),
                                                  (MIDX
                                                          ,LIM(231)),
      (MAXY
                            (MAXY1
                                   ,LIM(230)),
                                                                          000 6500
      (MIDY
4
             ,LIM(232)),
                            (MIN
                                                  (MINX
                                    ,LIM(233)),
                                                          ,LIM(234)),
                                                                          000 6600
5
      (MINX1 ,LIM(235)),
                            (MINY
                                    ,LIM(236)),
                                                  (MINY1
                                                          ,LIM(237)),
                                                                          000 6700
                                                  (MROW
      (MODEX ,LIM(238)),
                            (MPAD
                                    ,LIM(239)),
                                                          ,LIM(240)),
6
                                                                          000 6800
             ,LIM(241)),
                            (MXEL1 , LIM(242)),
                                                  (MX1
                                                          ,LIM(243)),
      (MXEL
                                                                          000 6900
a
      (MZA
             ,LIM(244)),
                                    ,LIM(245)),
                                                  (M1
                                                          ,LIM(246))
                            (MZB
                                                                          000 7000
 EQUIVALENCE
                                                                          000
                                                                              7100
                                                          ,LIM(255)),
1
      (NDXART, LIM(253)),
                            (NDX1
                                    ,LIM(254)),
                                                  (NDX5
                                                                          000
                                                                               7200
             ,LIM(256)),
                                    ,LIM(257)),
      (NDX3
                            (NDX4
                                                  (NEND
                                                          ,LIM(258)),
2
                                                                          000
                                                                               7300
3
      (NETOTX, LIM(259)),
                            (NEW
                                    , LIM(260)),
                                                  (NMIX
                                                          ,LIM(261)),
                                                                          000 7400
                                                  (NOINT
4
      (NNODE ,LIM(262)),
                            (NNODE1, LIM(263)),
                                                         ,LIM(264)),
                                                                          000 7500
5
      (NODEX ,LIM(265)),
                            (NODEX1, LIM(266)),
                                                  (NODEX2, LIM(267)),
                                                                          000
                                                                              7600
                                                  (NPT
             ,LIM(268)),
                                   ,LIM(269)),
                                                          ,LIM(270)),
      (NODX
                            (NPAD
                                                                          000
                                                                              7700
                                    ,LIM(272)),
      (NROW
             ,LIM(271)),
                            (NRX
                                                  (NRXM
                                                          ,LIM(273)),
                                                                          000
                                                                              7800
      (NRY
             ,LIM(274)),
                                                  (NRY1
                            (NRYM
                                   ,LIM(275)),
                                                          ,LIM(276)),
                                                                          000 7900
q
      (NRY2
             ,LIM(277)),
                            (NRY3
                                    ,LIM(278)),
                                                  (NRY4
                                                          ,LIM(279))
                                                                          000 8000
 EQUIVALENCE
                                                                          000
                                                                              8100
             ,LIM(280)),
      (NRY5
                            (NRY6
                                    ,LIM(281)),
                                                  (NRY7
1
                                                          ,LIM(282)),
                                                                          000 8200
      (NS
             ,LIM(283)),
                            (NSB
                                    ,LIM(284)),
                                                  (NSHX
                                                          ,LIM(285)),
                                                                          000 8300
-3
      (NSHY
             ,LIM(286)),
                            (NSL
                                    ,LIM(287)),
                                                  (NSTART, LIM(288)),
                                                                          000 8400
```

```
,LIM(289)), (NXX
                             ,LIM(290)), (NXL
,LIM(293)), (NX1
    4
        CNSU
                                              ,LIM(291)),
                                                          000 8500
                                              ,LIM(294)), 000 8600
    5
        (NXU
             ,LIM(295)), (NYT ,LIM(296)), (N1
        (NYB
                                               ,LIM(297))
                                                           000 8700
C
                                                           000 8800
C
                                                           000 9000
                                                           000 9100
C******
C
                                                           000 9200
¢
      ***
                                                           000 9300
C
      *
                                                           000 9400
Ċ
                                                           000 9500
     CALL INIT SUBROUTINE TO READ MODE DATA, CLEAR COMMON AND
                                                          000 9600
C
    INITIALIZE PROGRAM CONSTANTS AND PARAMETERS.
C
                                                           000 9700
                                                           000 9800
С
                                                           000 9900
     GO TO (100,1000,1000,1000,1000,9000), ISTART
                                                           000 0000
                                                          000 0100
Č
                                                          000 0300
     CALL INPUT FUNCTION TO READ IN DESIGN DATA AND PIN DATA.
C
                                                          000 0400
Ċ.
                                                          000 0500
C******
                                                          000 0600
C
      *
                                                          000 0700
C
     ***
                                                          000 0800
C
      *
                                                          000 0900
C
                                                          000 1000
 100 CALL INPUT
                                                          000 1100
    GO TO (2000,200,200,200,9000), IEND
                                                          000 1200
                                                          000 1300
000 1500
                                                          000 1600
000 1700
      *
     ***
                                                          000 1800
                                                          000 1900
                                                          000 2000
    CALL PLACE FUNCTION TO GENERATE A TWO DIMENSIONAL PLACEMENT.
                                                          000 2100
                                                          000. 2200
                                                          000 2300
000 2400
 200 IF(ITYPE.EQ.1) CALL PLEX1
    GO TO (9000,2000,300,300,9000), TEND
                                                          0.00 2500
000 2700
                                                          000 2800
000 2900
    CALL ROUTE TO GENERATE A TWO DIMENSIONAL ROUTING
   OF NODES ON THE CHIP.
                                                          000 3000
C******
                                                          000 3100
                                                          000 3200
000 3300
000 3400
000 3500
     ***
 300 IF(ITYPE.EQ.1) CALL RTEX1
                                                          000 3600
    GD TO (9000,9000,2000,400,9000), IEND
                                                          000 3700
                                                          000 3800
000 4000
    CALL ART TO WRITE ARTWORK INSTRUCTIONS TO THE OUTPUT TAPE.
                                                          000 4100
                                                          000 4200
                                                          000 4300
                                                          000 4400
  * ***
                                                          000 4500
```

```
С
                                                    000 4600
Ċ
                                                    000 4700
 400 IF(ITYPE.EQ.1) CALL ARTEX1
                                                    000 4800
    GO TO (9000,9000,9000,2000,9000), IEND
                                                    000 4900
                                                    000 5000
000 5200
                                                   000 5300
000 5400
    CALL READ TO RECOVER COMMON DATA STORED ON MAGNETIC TAPE. THEN
    GO TO PROPER RESTART FUNCTION.
C
                                                    000 5500
                                                    000 5600
********
Ĉ
                                                    000 5700
     *
C
     ***
                                                    000 5800
                                                    000 5900
     ×
                                                    000 6000
1000 CALL READ
                                                    000 6100
    GO TO (9000,200,300,400,3000,10000), ISTART
                                                    000 6200
                                                    000 6300
000 6500
    CALL WRITE TO STORE COMMON DATA ON MAGNETIC TAPE.
                                                    000 6600
                                                    000 6700
                                                    000 6800
     .
                                                    000 6900
                                                    000 7000
000 7100
     ***
     *
                                                    000 7200
2000 CALL WRITE
                                                    000 7300
    GO TO 10000
                                                    000 7400
CALL MANMOD FOR MAKING MANUAL CHANGES TO THE ARTWORK INSTRUCTIONS 000 7800
                                                    000 8000
                                                    000 8100
                                                    000 8200
     ***
C
     *
                                                    000 8300
t
                                                    000 B400
                                                    000 8500
3000 CALL MANEX1
    GO TO 2000
                                                    000 8600
                                                    000 8700
000 8900
C
    CALL DBCOM TO PRINT OUT COMMON DATA.
                                                    000 9000
                                                    000 9100
C*******
                                                    000 9200
                                                    000 9300
C
                                                    000 9400
C
     ***
Č
     *
                                                    000 9500
                                                    000 9600
                                                    000 9700
9000 CALL DBCOM
                                                    000 9800
C
                                                    000 0000
                                                   000 0100
000 0200
10000 STO
    END
CSTARTC INIT
               VER001 050674 1A
                                                    000-0100
    SUBROUTINE INIT
                                                    000 0200
                                                    000 0300
```

```
000 0500
Č
       THE PURPOSE OF THIS FUNCTION IS TO READ & VALIDATE THE MODE
                                                                               000 0600
C
       PARAMETERS, CLEAR COMMON AND PRESET PROGRAM CONSTANTS AND
                                                                               000 0700
       PARAMETERS.
¢
                                                                               000 0800
                                                                               000 0900
000 1100
C
       CHIP(IX):
                                                                               000 1200
C
                                                                               000 1300
                                                                               000 1400
C
           THIS ARRAY IS USED TO STORE CHIP DESCRIPTION DATA.
                                                                               000 1500
Ć
       ICAP(IX):
                                                                               000 1600
¢
                                                                               000 1700
           THIS ARRAY IS USED TO STORE THE NODE CAPACITANCE OF NODE IX.
                                                                               000 1800
000 1900
C
C
C
      INFO(IX, IY):
                                                                               000 2000
C
                                                                               000 2100
                                                                              000 2200
000 2300
           IX IS THE ELEMENT NUMBER ASSIGNED TO A DEVICE.
C
¢
           IY IS USED TO STORE DATA ASSOCIATED WITH ELEMENT IX.
                                                                              000 2400
C
                INFOY1 (=1) IS X-INDEX ON PLACEMENT SURFACE.
                                                                              000 2500
                                                                              000 2600
000 2700
                INFOY2 (=2) IS Y-INDEX ON PLACEMENT SURFACE.
Ç
                INFOY3 (#3) IN INPUT/PRPL1 USED TO STORE USER
C
C
                             SPECIFICATION OF POSITION OF FIXED ELEMENT
                                                                              000 2800
                             IN FIXED ROW.
                                                                              000 2900
                                                                              000 3000
000 3100
C
                             IN PLACE, IS ADDRESS (INDEX) IN LNAD(I)
                             OF LOWER LIMIT OF LIST OF ADDRESSES IN
C
                             NODE(I) OF THOSE NODES WHICH CONTAIN
                                                                              000 3200
C
C
                             ELEMENT IX.
                                                                              000 3300
C
                IN ROUTE, IS X-COORD. OF ELEMENT IX.
INFOY4 (=4) IN PLACE, IS UPPER LIMIT OF ADDRESSES IN
                                                                              000 3400
                                                                              000 3500
C
                             LNAD(I).
                                                                              000 3600
¢
                             IN ROUTE, IS Y-COORD. OF ELEMENT IX.
                                                                              000 3700
                INFOYS (=5) IN PLACE, IS BAD HISTORY COUNTER AND TEMPORARY STORAGE OF CELL WIDTH.
C
                                                                              000 3800
C
                                                                              000 3900
                             IN ROUTE, IS B.P. POSITION #, BY ROW TO
                                                                              000 4000
                LOCATE B.P. IN PROPER POSITION.
INFOY6 (=6) IS USED TO STORE ROW NUMBER AND PATTERN
C
                                                                              000 4100
C
                                                                              000 4200
t
                             ORIENTATION.
                                                                              000 4300
C
                             IN INPUT/PRPL1 USED TO STORE USER
                                                                              000 4400
                             SPECIFICATION OF ROW NUMBER OF ELEMENT
С
                                                                              000 4500
                             FIXED IN ROW AND/OP POSITION.
                                                                              000 4600
C
                INFOY7 (=7) IS USED TO STORE ADDRESS OF START OF PIN
                                                                              000 4700
                             DATA FOR PATTERN ASSIGNED TO ELEMENT IX.
                                                                              000 4800
                                                                              000 4900
      IP(IX) IS USED TO STORE PARAMETERS USED IN THIS PROGRAM:
                                                                              000 5000
           NOTE: ALL MIL VALUES ARE IN 10THS OF MILS, 10 = 1.0 MILS.
C
                                                                              000 5100
                EXCEPT FOR LINEAR WIDTH BREAK POINTS WHICH ARE IN MILS.
                                                                              000 5200
C
                                                                              000 5300
C
          IO IS THE STARTING INDEX FOR CONTROL PARAMETERS.
                                                                              000 5400
                                                                              700 5500°
C
                TP(TO
                        ) IS NUMBER OF IO DATA.
                                                                              000 5600
                IP(IO+ 1) IS NUMBER OF LINES PER PAGE PRINT CONTROL.
                                                                              000 5700
                IP(10+ 2) IS MAXIMUN NUMBER OF BONDING PADS ON CHIP.
                                                                              000 5800
                IP(IO+ 3) IS THE LINEAR WIDTH BREAK POINT FOR
                                                                   3 ROWS.
                                                                              000 5900
                IP(10+ 4) IS THE LINEAR WIDTH BREAK POINT FOR IP(10+ 5) IS THE LINEAR WIDTH BREAK POINT FOR
                                                                   4 ROWS.
C
                                                                              000 6000
                                                                   5 ROWS.
                                                                              000 6100
C
                             THE LINEAR WIDTH BREAK POINT FOR
                                                                   6 ROWS.
                IP(10+ 6) IS
                                                                              000 6200
                                                                   7 ROWS.
                IP(IO+ 7) IS THE LINEAR WIDTH BREAK POINT FOR
C
                                                                              000 6300
                                                                   8 ROWS.
                IP(IO+ 8) IS THE LINEAR WIDTH BREAK POINT FOR
С
                                                                              000 6400
                IP(IO+ 9) IS THE LINEAR WIDTH BREAK POINT FOR
                                                                  9 ROWS.
                                                                              000 6500
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IP(IO+10) IS THE LINEAR WIDTH BREAK POINT FOR 10 ROWS.
                                                               000 6600
IP(10+11) IS THE LINEAR WIDTH BREAK POINT FOR 11 ROWS.
                                                               000 6700
IP(10+12) IS THE LINEAR WIDTH BREAK POINT FOR 12 ROWS.
                                                               000 6800
IP(IO+13) IS SWITCH FOR END-AROUND WIRE SPACING
                                                               000 6900
           = 1, SPACED AT K100; = 2, SPACED AT K200.
                                                               000 7000
IP(10+14) IS TECHNOLOGY DEPENDENT SWITCH FOR GROUND
                                                               000 7100
           TO CELL ROWS.
                                                               000 7200
           = 1, GROUND BOTH SIDES VIA SHAPE SET.
                                                               000 7300
           = 2, GROUND BOTH SIDES VIA METAL.
                                                               000 7400
           = 3, GROUND LEFT SIDE VIA METAL.
                                                               000 7500
           = 4, SAME AS 3 PLUS VDD ON RIGHT SIDE.
                                                               000 7600
IP(10+15) IS THE MAXIMUM LINEAR WIDTH OF ALL CELLS
                                                               000 7700
           FOR USING THIS PROGRAM.
                                                               000 7800
P(IO+16) IS DESIGN DATA INPUT TAPE DSREF #, NOTO30.
                                                               000 7900
           IF DESIGN DATA ON CARD SET = 5.
                                                               000 8000
IP(IO+17) IS COMMON DATA OUTPUT TAPE DSREF #, NOTO35.
                                                               000 8100
IP(IO+18) IS COMMON DATA INPUT TAPE DSREF #, NOTO40.
                                                               000 8200
IP(10+19) IS PIN DATA FILE INPUT TAPE DSREF #, NOTO45.
IP(10+20) IS PIN DATA FILE OUTPUT TAPE DSREF #, NOTO50.
                                                               000 8300
                                                               000 8400
IP(I0+21) IS WORK TAPE # 1 DSREF #, NOTO10.
                                                               000 8500
IP(10+22) IS WORK TAPE # 2 DSREF #, NOTO15.
IP(10+23) IS WORK TAPE # 3 DSREF #, NOTO20.
                                                               000 8600
                                                               000 8700
IP(I0+24) IS WORK TAPE # 4 DSREF #, NOTO25.
                                                               000 8800
IP(10+25) IS ARTWORK INST. OUTPUT TAPE # 1 DSREF NOT055. 000 8900 IP(10+26) IS ARTWORK INST. OUTPUT TAPE # 2 DSREF NOT060. 000 9000
IP(I0+27) IS FIX1 CONTROL.
                                                               000 9100
           = 0, FIX B.P. AND ROWS IN ONE PASS
                                                               000 9200
           = 1, FIX B.P. ON FIRST PASS, FIX ROWS ON
                                                               000 9300
                SECOND PASS.
                                                                000 9400
IP(10+28) IS BYPASS CONTROL FOR CONVERTING CENTER
                                                               000 9500
           TRANSITION TUNNELS, WITH METAL ON BOTH ENDS
                                                               000 9600
           OF TUNNEL, TO METAL.

IF < 0 AND IP(14+99) > 0, DO NOT CONVERT AND
                                                               000 9700
                                                               000 9800
                ROUTE VDD ON CELL ROW.
                                                               000 9900
           IF = 0, CONVERT & NO VDD BETWEEN ROWS.
                                                               000 0000
           IF > 0, NOT CONVERT & PUT VDD BETWEEN ROWS.
                                                               000 0100
IP(I0+29) IF NUMBER OF LEVELS OF ARTWORK.
                                                               000 0200
IP(10+30) IS BYPASS CONTROL TO FORCE ROWS TO BE
                                                               000 0300
           EVEN IN FIX1.
                                                               000 0400
           = 0, IF NECESSARY, FORCE ELEM FROM LONG ROW
                                                               000 0500
                INTO SHORT ROW.
                                                               000 0600
                MOVE ELEM FROM LONG ROW INTO OPEN SLOT
                                                               000 0700
                OF SHORT ROW.
                                                               000 0800
           = 2, BYPASS EQUALIZING ROWS.
                                                               000 0900
IP(I0+31) IS TECHNOLOGY DEPENDENT SWITCH FOR GENERATING
                                                               000 1000
           SECOND LEVEL TUNNELS.
                                                               000 1100
           = 0, NO SECOND LEVEL; > 0, GENERATE TUNNELS
                                                               000 1200
           ON LEVEL IP(I0+31).
                                                               000 1300
IP(10+32) IS TECHNOLOGY DEPENDENT CONTROL FOR SPECIFYING
                                                               000 1400
           METAL LEVEL.
                                                               000 1500
IP(IO+33) IS TECHNOLOGY DEPENDENT CONTROL FOR SPECIFYING
                                                               000 1600
PRIMARY TUNNEL LEVEL.

IP(10+34) IS TECHNOLOGY DEPENDENT BORDER TYPE:
                                                               000 1700
                                                               000 1800
           = 1, BULK C-MOS BONDED WIRE.
                                                               000 1900
           = 2, BULK C-MOS BEAM LEAD.
                                                                000 2000
           = 3, POLY BONDED WIRE.
                                                               000 2100
           = 4. POLY BEAM LEAD.
                                                               000 2200
           = 5, SOS BONDED WIRE.
                                                               000 2300
           = 6. SOS BEAM LEAD.
                                                               000 2400
IP(10+35) IS BYPASS CONTROL IN ADJUST TO PREVENT
                                                               000 2500
           FORCING CHIP TO BE BALANCED.
                                                               000 2600
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000 2700
            = 0, BALANCE CHIP IN X.
            = 1, BYPASS ADJA1
                                                                    000 2800
IP(10+36) IS BYPASS CONTROL FOR ALTERNATE INPUT PATTERN
                                                                   000 2900
            OF B.P. ON SIDE OF CHIP
= 0, SUBSTITUTE ALTERNATE PATTERN
                                                                    000 3000
                                                                    000 3100
            = 1, USE SAME PATTERN
                                                                    000 3200
IP(I0+37) IS POLY TECHNOLOGY FOR LEVEL 10 GUARD.
                                                                    000 3300
            = 0, NO SECOND TUNNEL OR SHAPE SET.
                                                                    000 3400
000 3500
            = 1, GENERATE SECONDARY LEVEL OF TUNNELS.
= 2, GENERATE SHAPE SET FOR NON-CELL AREA.
                                                                    000 3600
IP(I0+38) IS TOP ROW (IF ODD #) GROUND FROM SIDE BYPASS. 000 3700
            = 0, GND ON TOP, BYPASS SIDE GROUND.
                                                                    000 3800
            = 1, NOT ON TOP, GENERATE ON SIDES VIA IP(10+14) PARAMETER.
                                                                    000 3900
                                                                    000 4000
IP(I0+39) IS CONTROL TO FORCE B.P. TO BE RELOCATED ON
                                                                    000 4100
            BONDING PAD ROWS, SO THAT ROUTED WIRE WILL
                                                                    000 4200
            MAKE POSITIVE CONTACT WITH B.P. PIN.
                                                                    000 4300
            = 0, DO NOT MOVE B.P.
                                                                    000 4400
            > 0, ADJUST POSITION OF B.P. (NOT MORE THAN
                                                                    000 4500
            IP(10+39) UNITS.
                                                                    000 4600
IP(I0+40) IS T.E. AT ZERO ROUTING CHANNEL CONTROL.
                                                                    000 4700
                                                                    000 4800
            = 0, NO T.E. IN ZERO CHANNEL.
            = 1, T.E. FOR CELLS ONLY.
= 2, T.E. FOR CELLS & BONDING PADS.
                                                                    000 4900
                                                                    000 5000
IP(10+41) IS ZERO CHANNEL ROUTING CONTROL.
                                                                    000 5100
            = 0, NO ZERO CHANNEL ROUTING.
                                                                    000 5200
            = 1, ROUTE CLASS 1 AT ZERO CHANNEL.
= 2, ROUTE CLASS 1 & 2 AT ZERO CHANNEL.
                                                                   000 5300
000 5400
            = 3, ROUTE CLASS 2 AT ZERO CHANNEL.
                                                                    000 5500
IP(10+42) > 0, IS PAUSE STATEMENT BYPASS.
                                                                   000 5600
IP(10+43) IS THE LINEAR WIDTH BREAK POINT FOR 13 ROWS.
                                                                    000 5700
IP(10+44) IS THE LINEAR WIDTH BREAK POINT FOR 14 ROWS. IP(10+45) IS THE LINEAR WIDTH BREAK POINT FOR 15 ROWS.
                                                                   000 5800
000 5900
                                                                    000 6000
IP(10+46) IS THE LINEAR WIDTH BREAK POINT FOR 16 ROWS.
IP(10+47) IS THE LINEAR WIDTH BREAK POINT FOR 17 ROWS.
                                                                    000 6100
IP(10+48) IS THE LINEAR WIDTH BREAK POINT FOR 18 ROWS.
                                                                    000 6200
IP(10+49) IS THE LINEAR WIDTH BREAK POINT FOR 19 ROWS.
                                                                    000 6300
IP(I0+50) IS THE LINEAR WIDTH BREAK POINT FOR 20 ROWS.
                                                                    000 6400
IP(10+51) IS PLACE SURFACE OPTION CONTROL:
                                                                   000 6500
            = 0, C-MOS/SI-GATE OPTION: CXXXCXCXXXC...
                                                                    000 6600
            = 1, SOS OPTION: CXXCCXXCCXXCC...
                                                                    000 6700
IP(10+52) > 0 TEST CELL PIN-TO-PIN MULTIPLE OF K200. IP(10+53) > 0, INTERCHANGE GND/VDD DATA. IF IP(10+54)
                                                                    000 6800
                                                                    000 6900
                 > 1, SET IP(10+53)=1, IF < 2, SET = 0.
                                                                   000 7000
IP(10+54) IS GND/VDD LOCATION CONTROL.
                                                                   000 7100
            = 0, GND BELOW CENTER - VDD ABOVE.
                                                                   000 7200
            = 1, GND ABOVE CENTER - VDD BELOW.
                                                                   000 7300
              2, PADS IN CORNNERS, VDD BOTTOM-LEFT, GND
                                                                   000 7400
                  TOP-RIGHT.
                                                                   000 7500
            = 3, PADS IN CORNNERS. VDD TOP-LEFT, GND
                                                                   000 7600
                  BOTTOM-RIGHT.
                                                                   000 7700
IP(10+55) > 0, ROUTE SHORTEST CLASS 1 NODES FIRST. IP(10+56) > 0, ROUTE CLASS 2 ON ZERO CHANNEL IF IT IS
                                                                   000 7800
                                                                   000 7900
                  THE END PIN. BYPASS IF IP(10+41) > 1.
                                                                   000 8000
IP(I0+57) > 0, BYPASS REROUTE OF LOW-PROFILE METAL.
                                                                   000 8100
BYPASS IF IP(14+99) = 0.
IP(10+58) > 0, ROUTE SHORTEST CLASS 5 FIRST.
                                                                   000 8200
                                                                   000 8300
                B.P. ROWS ONLY
ALL ROWS.
                                                                   000 8400
            = 1
                                                                   000 8500
            > 0 IS ODD CELL ROW TO TOP B.P. MODIFIED 000 8600 ROUTE (CLASS 2 INSTEAD OF CLASS 3) BYPASS. 000 8700
IP(I0+59) > 0
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= 0, EXERCISE
                                                                              000 8800
       IP(10+60) IS SPECIAL ROUTE FOR CLASS 3 WHERE ROUTE IS
                                                                              000 8900
                   BACK-TO-BACK ROW PLUS B.P. THE OPTION, > 0, WILL SMASH NODE INTO CLASS 2 + CLASS 6.
                                                                               000 9000
                                                                              000 9100
                   > 0, EXERCISE OPTION, WHERE IP(10+60) IS THE
                                                                               000 9200
                          NUMBER OF THESE PER SIDE ON ROW PAIR.
                                                                              000 9300
                   = 0, BYPASS.
                                                                               000 9400
       IP(I0+61) > 0, CALCULATE CROSSOVERS FOR EACH NODE.
                                                                              000 9500
                   FOR GROUND VIA TUNNEL, = # EQUIV. CROSSOVERS.
                                                                              000 9600
       IP(10+62) IS ALTERNATE LOCATION OF POWER TO B.P.
                                                                              000 9700
                   SEE IP(12+19).
                                                                              000 9800
       IP(10+63) > 0, PRINT UNUSED PIN LIST IN INPUT.
                                                                              000 9900
       IP(10+64) = 0, BYPASS PIN-TO-TAP IN CENTER ROUTING IF
                                                                              000 0000
                   TAP POINT NOT WITHIN ROW LIMITS.
                                                                               000 0100
                   > 0, BYPASS THIS OPTION.
                                                                              000 0200
       IP(10+65) IS SIDE BONDING PAD ROUTING CONTROL.
                                                                               000 0300
                   = 0, BYPASS THIS FEATURE.
                                                                              000 0400
                   = 1, PREVENT ROUTE FROM SIDE B.P. TO BE WITHIN 000 0500 PREVEOUSLY ROUTED ROW-TO-ROW ROUTING. 000 0600
                   = 2, SAME AS 1 + DISABLE PIN-TO-TAP ROUTE OF
                                                                              000 0700
                          SIDE B.P.
                                                                              000 0800
                   IF > 0, RESET IP(14+96) = 0.
                                                                              000 0900
       IP(I0+66) > 0, LIMIT LOW PROFILE METAL TO START/END IN
                                                                              000 1000
                          VERTICAL CHANNEL WHICH DOES NOT CONTAIN
                                                                              000 1100
                          A TUNNEL.
                                                                              000 1200
       IP(I0+67) RE-ROUTE MULTIPLE PINS ON SAME CELL ROW
                                                                             000 1300
                   TO LOWER HORIZONTAL CHANNEL CONTROL.
                                                                              000 1400
      = 0, EXECUTE; > 0, BYPASS.

IP(I0+68) > 0, BYPASS PRINT OF OUTPUT DATA.
                                                                              000 1500
000 1600
      IP(10+68) > 0, BYPASS PRINT OF OUTPUT DATA.

IP(10+69) = 0, FORCE, IF POSSIBLE, ODD CELL ROW TO
TOP B.P. (VIA CLASS 2, SEE IP(10+59))
TO BE ALL METAL AT CELL ROW.
IF IP(10+59) > 0, RESET IP(10+69) = 1.

IP(10+70) IS ROW 3/NROW B.P. ROUTING CONTROL.

= 0, FORCE ROUTE TO B.P. ORDER AND ROUTE
SINGLE STEP NOT TO CROSS EACH OTHER.
DOUBLE STEP IS NORMAL ROUTE.
                                                                              000 1700
                                                                              000 1800
                                                                              000 1900
000 2000
                                                                              000 2100
                                                                              000 2200
                                                                              000 2300
                                                                              000 2400
                   IF IP(I4+99) = 0, FORCE IP(I0+70) = 1.
                                                                              000 2500
                   > 0, BYPASS
                                                                              000 2600
      IP(IU+71) IS CONTROL TO OVERRIDE CERTAIN USER OPTIONS.
                                                                              000 2700
                   SEE IMPUT SUBROUTINE.
                                                                              000 2800
                   = 0, EXERCISE.
                                                                              000 2900
                   > 0, BYPASS THIS CONTROL.
                                                                              000 3000
      IP(I0+72) IS EXTERNAL SORT CONTROL.
                                                                              000 3100
                   = 0, BYPASS.
                                                                              u00 3200
                   > 0, EXECUTE EXTERNAL SORT OF OUTPUT DATA.
                                                                              000 3300
                         SEE SORT1 AND ART FUNCTION.
                                                                              000 3400
                                                                              000 3500
II IS THE STARTING INDEX FOR PLACEMENT PARAMETERS.
                                                                              000 3600
                                                                              000 3700
               ) IS NUMBER OF I1 DATA.
                                                                              000 3800
      IP(I1+ 1) IS JRSP SWITCH INITIAL VALUE.
                                                                              000 3900
      IP(I1+ 2) IS JSL SWITCH INITIAL VALUE. IP(I1+ 3) IS JBAD LIMIT.
                                                                              000 4000
                                                                              000 4100
      IP(I1+ 4) IS LIMINT VALUE.
                                                                              000 4200
      IP(II+ 5) IS LE INITIAL VALUE.
                                                                              000 4300
      IP(II+ 6) IS FACTOR USED TO DETERMINE LE IF IP(II+5) = 0 000 4400 IP(II+ 7) IS LOWER LIMIT OF LE WHEN IP(II+5) = 0. 000 4500
      IP(II+ 8) IS LX SCALING FACTOR.
                                                                              000 4600
      IP(I1+ 9) IS LY SCALING FACTOR.
                                                                              000 4700
      IP(I1+10) IS NMIX VALUE.
                                                                              000 4800
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IP(I1+11) IS JSWC SWITCH VALUE.
                                                                   000 4900
     IP(11+12) IS LE INCREMENT VALUE. IP(11+13) IS LE DECREMENT VALUE.
                                                                   000 5000
000 5100
     IP(I1+14) IS PLACEMENT PRINT CONTROL.
                                                                  000 5200
     IP(I1+15) IS THE BAD HISTORY INCREMENT VALUE
                                                                   000 5300
     IP(I1+16) IS THE BAD HISTORY DECREMENT VALUE IP(I1+17) IS THE DUMMY NODE (FOR SUBDIVIDED CELLS)
                                                                  000 5400
                                                                  000 5500
                WEIGHTING FACTOR.
                                                                  100 5600
     IP(I1+18) IS NODE WEIGHTING FACTOR FOR INPUT B.F.
                                                                  000 5700
     IP(I1+19) IS NODE WEIGHTING FACTOR FOR OUTPUT B.P.
                                                                  000 5800
     IP(11+20) IS FACTOR USED IN DETERMINING UNIT AREA.
                                                                  000 5900
     IP(I1+21) IS PRESET UNIT AREA.
                                                                  000 6000
                                                                  000 6100
     IP(I1+22) IS USED TO ADJUST ROUNDOFF FACTOR FOR
                SUBDIVISION OF LONG CELLS.
                                                                  200 6200
                IS PRESET ROUNDOFF FOR CELL SUBDIVISION.
     IP(I1+23)
                                                                  000 6300
     IP(I1+24) IS NUMBER OF UNIT AREAS BETWEEN FACING ROWS.
                                                                  000 6400
000 6500
                MUST BE > 1
     IP(I1+25) IS NUMBER OF UNIT AREAS BACK-TO-BACK ROWS.
                                                                  000 6600
     IP(I1+26) IS NUMBER OF UNIT AREAS ON LEFT SIDE OF ALL
                                                                  000 6700
                BACK-TO-BACK ROWS.
                                                                  000 6800
                                                                  000 6900
     IP(I1+27) IS NUMBER OF UNIT AREAS ON RIGHT SIDE OF ALL
                BACK-TO-BACK ROWS.
                                                                  000 7000
     IP(I1+28) IS NUMBER OF SLOTS RESEARVED FOR PHOTO KEY ON "
                                                                  000 7100
                BOTTOM/TOP B.P. ROWS.
                                                                  000 7200
                                                                  000 7300
     IP(I1+29) IS NUMBER OF SLOTS RESEARVED FOR TEST TRAN-
                ISTERS ON SIDE B.P. ROWS.
                                                                  000 7400
     IP(I1+30) IS DEFAULT AVERAGE CELL WIDTH.
                                                                  000 7500
                                                                  000 7600
000 7700
     IP(I1+31) IS JNTG LIMIT CONTROL. JNTG MUST BE GREATER
                THAN IP(I1+31) TO ALLOW NEW TRY AT PLACEMENT.
     IP(I1+32) IS BYPASS CONTROL ON INTERCHANGE OF ELEMENTS
                                                                  000 7800
                ON EQUALITY IN PLACE.
                                                                  000 7900
                = 0 EXECUTE INTERCHANGE ON EQUALITY.
                                                                  000 8000
                     BYPASS INTERCHANGE ON EQUALITY.
                = 1
                                                                  000 8100
     IP(I1+33) IS ODD CELL ROW EXTRA UNIT AREAS.
                                                                  000 8200
                                                                  000 8300
12 IS THE STARTING INDEX FOR ROUTING PARAMETERS.
                                                                  000 8400
                                                                  000 8500
             ) IS NUMBER OF 12 DATA.
     IP(I2
                                                                  200 8600
     IP(I2+ 1) IS THE METAL-TO-METAL CHANNET SPACING ON
                                                                  000 8700
                THE ROUTING SURFACE.
                                                                  000 8800
     IP(I2+ 2) IS THE PIN+TO-PIN SPACING ON ROUTING
                                                                  000 8900
                SURFACE.
                                                                  000 9000
     IP(12+ 3) IS BONDING PAD TO BONDING PAD SPACING.
                                                                  000 9100
     IP(I2+ 4) IS DISTANCE FROM CHIP CENTER TO START OF
                                                                  000 9200
               BONDING PADS ON HORIZONTAL ROWS OF B.P.
                                                                  000 9300
               INITIALLY SET = B.P. ORIGIN TO CENTER OF PAD.
                                                                 000 9400
     IP(12+ 5) IS DISTANCE FROM CHIP CENTER TO START OF
                                                                  000 9500
               BONDING PADS ON VERTICAL ROWS OF B.P.
                                                                 000 9600
               INITIALLY SET = B.P. ORIGIN TO CENTER OF PAD,
                                                                 000 9700
               MINUS ADDITIONAL OFFSET FOR TEST TRANSISTOR.
                                                                 000 9800
     IF(12+ 6) IS AN ARBITRARY X/Y VALUE OF CENTER OF CHIP.
                                                                 000 9900
     IP(12+ 7) IS NUMBER OF TUNNEL CHANNELS TO BE GUARDED.
                                                                 0000 000
     IP(12+ 8) IS THE Y-DISTANCE OF THE SIDE GROUND FROM
                                                                 000 0100
               THE CELL ROW ORIGIN.
                                                                 000 0200
     IP(12+ 9) IS THE NUMBER OF INDICIES PER STEP USED TO
                                                                 000 0300
               INTERCONNECT B.P. TO NODE IN ADJUSTMENT
                                                                 000 0400
               OF CENTER ROUTING SURFACE DUE TO X SHIFT.
                                                                 000 0500
    IP(12+10) IS HEIGHT OF CELLS.
                                                                 000 0600
    IP(IZ+11) IS Y-DISTANCE FROM ORIGIN OF CELL TO CENTER
                                                                 000 0700
               OF PIN.
                                                                 000 0800
    IP(12+12) IS Y-DISTANCE FROM ORIGIN OF CELL TO FIRST
                                                                 000 0900
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		METAL CHANNEL.	000	1000
	IP(12+13)	IS DISTANCE OF FIRST RIGHT END-AROUND VERTICAL	000	1100
		CHANNEL FROM EDGE OF HORIZONTAL CELL ROWS.		1200
	IP(12+14)	IS MINIMUM DISTANCE OF FIRST TUNNEL VERTICAL	000	1300
	•	CHANNEL FROM LAST END-AROUND CHANNEL.	000	1400
•	IP(I2+15)	IS THE MINIMUM DISTANCE FROM ORIGIN OF B.P.	000	1500
1		TO NEAREST NON-METAL DEVICE (TUNNEL OR CELL).	000	1600
	IP(I2+16)	IS Y-DISTANCE FROM ORIGIN OF B.P. TO FIRST	000	1700
		METAL CHANNEL.	000	1800
	IP(12+17)	IS MINIMUM NUMBER OF WIRING CHANNELS FOR	000	1900
		CHANGING TUNNEL TO METAL.	000	2000
	IP(I2+18)	IS B.P. SHIFT (CCW) FOR BEAM LEAD TECHNOLOGY.	000	2100
	IP(I2+19)	IS Y-DISTANCE FROM B.P. ORIGIN TO HORIZONTAL	000	2200
F.	•	GROUND LINE TO CELL KOWS.	000	2300
1	IP(I2+20)	IS ADJUSTMENT CONSTANT FOR SIDE ROUTING	000	2400
	•	X-INCREMENT. USED TO SQUEEZE SIDE CHANNELS	000	2500
<del></del>		CLOSER TO EACH OTHER (SHOULD NOT BE GREATER	000	2600
		THAN 1 OR LESS THAN 0).	000	2700
1	IP([2+21)	IS CORRECTION FOR FIRST SIDE ROUTING CHANNEL.	000	2800
	Ib(15+55)	IS CONTROL FOR MINIMIZING CHANNEL SPACING ON	000	2900
	-	SIDE ROUTING SURFACE. SEE POS1.	000	3000
1	•	= 0, MINIMIZE.	000	3100
		= 1, DO NOT MINIMIZE.	000	3200
•	IP(I2+23)	IS CONTROL FOR CELL REORIENTATION AND PIN	000	3300
		REASSIGNMENT. SEE SMASH1.	000	3400
		= 0 EXECUTE.	000	3500
1		> 0 BYPASS.		3600
•	IP(I2+24)	IS BYPASS CONTROL ON ORDER OF NROW B.P. ROUTE.	000	3700
	•	SEE SMASH1.	000	3800
•		= 0, FORCE ROUTE TO B.P. ORDER.	000	3900
<del>-</del> ;-		= 1, BYPASS THIS FORCED ROUTE ORDER.	000	4000
T.	IP(12+25)	IS BYPASS CONTROL POSSIBLE REDUCTION OF CLASS		4100
1		3 NODE INTO CLASS 2 & 5, WITH POSSIBLE	000	4200
1		SMALLER CLASS 3 NODE SEGMENT. SEE SMASH1.		4300
· <del>-</del>		= 0, EXECUTE SUBDIVISION OF CLASS 3 NODE.	000	4400
		= 1, BYPASS EXECUTION.	-	4500
	Ib(15+59)	IS B.P. REPOSITIONING SWITCH (POPL1).	000	4600
	21	= 0, BYPASS THIS FEATURE.	000	4700
		= 1, REPOSITION PADS ON ALL SIDES.		4800
•		= 2, ALSO SHIFT ALL PADS ON ROW TOWARD CENTER.	-	
·		= 3, ALSO INTERCHANGE PADS BETWEEN ROWS.		5000
		= 4, SAME AS 1 & 3, BUT NOT 2.		5100
	IP(I2+27)	IS BYPASS CONTROL ON LSI PLACEMENT ALGORITHM		5200
		WITH RESPECT TO MINIMUM DISTANCE TO NEAREST		5300
•		NEIGHBOR ALGORITHM, SEE PLACE/DIST.		5400
		= 0, EXECUTE LSI DIST.		5500
:		= 1, EXECUTE MINIMUM DIST. NEAREST NEIGHBOR.	-	5600
	*0(*3:30:	= 2, EXECUTE DRIVER CENTERED LSI DIST.		5700
•	Th/15450)	IS BYPASS CONTROL ON RESORT OF LINE DATA		5800
		IN SORTI.		5900
-		= 0, EXECUTE RESORT.		6000
	TD ( T 2 : 20 :	= 1, BYPASS.		6100
-	Th(15+54)	IS EXEC/BYPASS CONTROL ON STEP 2B IN POC1.		6200
		= 0, EXECUTE FOR ALL CENTER ROWS.		6300
:		= 1, EXECUTE ONLY FOR B.P. ROWS.		6400
<del></del>	TD( T3 - 70 *	= 2, BYPASS,		6500
j.	TE(15+20)	IS Y-DISTANCE FROM ORIGIN OF B.P. TO CENTER		6600
- <del></del> -	TD(T2:71)	OF PIN.		6700
•	TE(15421)	IS DISTANCE OF FIRST LEFT END-AROUND VERTICAL		6800
: '		CHANNEL FROM EDGE OF HORIZONTAL CELL ROWS.		6900
· — — · · ·		IF $IP(I0+14) = 3$ , SET $IP(I2+13) = IP(I2+1)$	000	7000

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IP(12+32) IS MINIMUM NUMBER OF WIRING CHANNELS FOR
                                                                            000 7100
                   CHANGING TUNNEL AT PIN TO METAL. IP(12+17)
                                                                            000 7200
                                                                            000 7300
                  MUST ALSO BE > 0.
      IP(I2+33) IS SPACING CONSTANT USED TO ADJUST FIRST
                                                                            000 7400
                  METAL CHANNEL FROM ODD CELL ROW.
                                                                            000 7500
                                                                            000 7600
IS THE STARTING INDEX FOR ROW RELATED DATA. THESE DATA ARE STORED ROW BY ROW, IN ROW ORDER. DATA FOR ROW 2
                                                                            000 7700
                                                                            000 7800
      FOLLOWS ROW 1 DATA, ETC. SAME DATA FOR EACH ROW MUST
                                                                            000 7900
      BE IN SAME ORDER. IP(13) IS THE NUMBER OF DATA STORED
                                                                            000 8000
      FOR EACH ROW. TO EXTRACT, FOR EXAMPLE, DATA ITEM 3
FOR ROW 6, M3 = I3 + (6-1) * IP(I3) + 3
                                                                            000 8100
                                                                            000 8200
                                                                            000 8300
                ) IS THE NUMBER OF INDICIES FOR EACH ROW DATA.
      IP(I3
                                                                            000 8400
      IP(M3+ 1) IS THE Y-COORDINATE OF ROW (X FOR ROW 1/2).
                                                                            000 8500
      IP(M3+ 2) IS THE NORMAL CELL ORIENTATION FOR ROW.
                                                                            000 8600
      IP(M3+ 3) IS THE REVERSE CELL ORIENTATION FOR ROW.
                                                                            000 8700
      IP(M3+ 4) IS THE NO. OF LEFT END-AROUND WIRES FOR ROW.
                                                                            000 8800
      IP(M3+ 5) IS THE NO. OF RIGHT END-AROUND WIRES FOR ROW.
                                                                            000 8900
      IP(M3+ 6) IS THE X-INDEX OF LEFT END OF ROW(Y FOR 1/2). 000 9000 IP(M3+ 7) IS THE X-COORD OF LEFT END OF ROW (Y FOR 1/2). 000 9100
      IP(M3+ 8) IS THE X-INDEX OF FIRST LEFT END-AROUND NODE. 000 9200 IP(M3+ 9) IS THE X-INDEX OF FIRST RIGHT END-AROUND NODE. 000 9300
      IP(M3+10) IS THE X-INDEX OF RIGHT END OF ROW(Y FOR 1/2). 000 9400 IP(M3+11) IS THE WIDTH OF ALL CELL ON ROW. 000 9500
      IP(M3+12) IS X-COORD OF FIRST LEFT END-AROUND NODE.
                                                                            000 9600
      IP(M3+13) IS X-COORD OF FIRST RIGHT END-AROUND NODE.
                                                                            000 9700
      IP(M3+14) IS OPTION IP(10+60) COUNTER. IF ROW IS EVEN, 000 9800 COUNT IS # TO SIDE ROW 1. IF ROW IS ODD, 000 9900 COUNT IS # TO SIDE ROW 2. SEE OPTION IP(10+60) 000 0000
      FOR FURTHER DETAILS.

IP(M3+15) PLACEMENT Y-INDEX OF ROW (X FOR ROW 1/2).
                                                                           000 0100
                                                                           000 0200
      IP(M3+16) PLACEMENT LEFT X-INDEX OF ROW (Y FOR 1/2).
IP(M3+17) PLACEMENT RIGHT X-INDEX OF ROW(Y FOR 1/2).
                                                                           000 0300
                                                                           000 0400
      IP(M3+18) PLACEMENT NON-VALID ROW Y-INDEX.
                                                                           000 0500
      IP(M3+19) IS X-COURD. OF LAST LEFT END-AROUND NODE.
                                                                           000 0600
      IP(M3+20) IS X-COORD. OF LAST RIGHT END-AROUND NODE.
                                                                           000 0700
                                                                           000 0800
14 IS THE STARTING INDEX FOR CHIP/ARTWORK PARAMETERS.
                                                                           000 0900
                                                                           000 1000
      IP(14 - ) IS NUMBER OF 14 DATA.
                                                                           000 1100
      IP(14+ 1) IS X STEP & REPEAT, INCREMENT OF IP(14+91) IP(14+ 2) IS Y STEP & REPEAT, INCREMENT OF IP(14+91)
                                                                           000 1200
                                                                           000 1300
      IP(14+ 3) IS ONE HALF WIDTH OF STREET.
                                                                           000 1400
      IP(14+ 4) IS Y-DISTANCE FROM CENTER OF CHIP RESERVED
                                                                           000 1500
                  FOR TEST RESISTOR.
                                                                           000 1600
      IP(I4+ 5) IS Y+DISTANCE ABOVE CENTER OF CHIP TO TOP
                                                                           000 1700
                                                                           000 1800
                  OF TEST RESISTOR BONDING PAD.
      IP(14+ 6) IS Y-DISTANCE ABOVE CENTER OF CHIP TO BOTTOM
                                                                           000 1900
      OF TEST RESISTOR B.P.

IP(14+ 7) IS 1/2 WIDTH OF TEST RESISTOR.
                                                                           000 2000
                                                                           000 2100
      IP(14+ 8) IS MINIMUM Y STEP & REPEAT FOR PHOTO KEY TO
                                                                           000 2200
                  BE LOCATED WITHIN MASKING BORDER.
                                                                           000 2300
      IP(14+ 9) IS WIDTH OF MASKING BORDER.
                                                                           000 2400
      IP(14+10) IS WIDTH OF N+(NOT) INSIDE STREET.
                                                                           000 2500
      IP(I4+11) IS 1/2 WIDTH/HEIGHT OF PHOTO KEY.
                                                                           000 2600
      IP(14+12) IS Y-DISTANCE FROM CENTER OF STREET TO
                                                                           000 2700
                  CENTER OF PHOTO KEY, KEY INSIDE CHIP.
                                                                           000 2800
      IP(14+13) IS X-DIST OF GND PAD FROM CENTER OF STREET.
                                                                          000 2900
      IP(14+14) IS Y-DIST OF GND PAD BELOW CENTER OF CHIP.
                                                                          000 3000
                  ALSO Y-DIST. CORRECTION FOR IP(10+54) > 1
                                                                           000 3100
```

```
IP(14+15) IS ORIENTATION OF GND PATTERN.
                                                                      000 3200
IP(14+16) IS X-DIST OF VDD PAD FROM CENTER OF STREET.
                                                                      000 3300
IP(14+17) IS Y-DIST OF VDD PAD ABOVE CENTER OF CHIP.
            ALSO Y-DIST. CORRECTION FOR IP(10+54) > 1
                                                                      000 3500
IP(I4+18) IS ORIENTATION OF VDD PATTERN.
                                                                      000 3600
IP(14+19) IS X-DIST OF P-TT FROM CENTER OF STREET, IP(14+20) IS Y-DIST OF P-TT ABOVE CENTER OF CHIP.
                                                                      000 3700
                                                                      000 3800
IP(14+21) IS ORIENTATION OF P-TT PATTERN.
                                                                      000 3900
IP(14+22) IS X-DIST OF N-TT FROM CENTER OF STREET.
                                                                      000 4000
IP(14+23) IS Y-DIST OF N-TT ABOVE CENTER OF CHIP.
                                                                      000 4100
IP(14+24) IS ORIENTATION OF N-TT PATTERN.
IP(14+25) IS X/Y-DIST OF CHIP ALIGNMENT KEYS FROM
                                                                      000 4200
                                                                      000 4300
            STREET OR FROM EACH OTHER.
                                                                      000 4400
IP(14+26) = 0, CHIP IS SQUARE, > 0 FOR RECTANGULAR.
                                                                      000 4500
IP(14+27) IS X-DIST OF RCA PATTERN FROM CENTER OF STREET 000 4600
               = 0, BYPASS RCA PATTERN.
                                                                      000 4700
IP(14+28) IS Y-DIST OF RCA PATTERN FROM CENTER OF STREET 000 4800
IP(14+29) IS ORIENTATION OF RCA PATTERN.
                                                                      000 4900
IP(14+30) IS X-DIST OF ATL PATTERN FROM CENTER OF STREET 000 5000 IP(14+31) IS Y-DIST OF ATL PATTERN FROM CENTER OF STREET 000 5100 IP(14+32) IS ORIENTATION OF ATL PATTERN. 000 5200
IP(14+33) IS Y-DIST OF 1ST DIGIT ID FROM CENTER STREET.
                                                                      000 5300
IP(14+34) IS X-INCREMENT BETWEEN DIGITS OF ID.
IP(14+35) IS X-OFFSET OF CHIP FROM NORMAL CHIP ORIGIN.
IP(14+36) IS Y-OFFSET OF CHIP FROM NORMAL CHIP ORIGIN.
                                                                      000 5400
                                                                      000 5500
                                                                      000 5600
IP(14+37) IS X-POSITION (ABSOLUTE) OF START OF SYMBOL
                                                                      000 5700
            DATA FROM LOWER LEFT CORNER STREET CENTER;
                                                                      000 5800
            MODIFIED BY CHIP SCALE FACTORS.
                                                                      000 5900
IP(14+38) IS Y-POSITION (ABSOLUTE) OF START OF SYMBOL
                                                                      000 6000
            DATA BELOW LOWER LEFT CORNER STREET CENTER;
                                                                      000 6100
            MODIFIED BY CHIP SCALE FACTORS.
                                                                      000 6200
IP(14+39) IS 1/2 X-STEP
IP(14+40) IS 1/2 Y-STEP
IP(14+41) IS HALF WIDTH OF LEVEL 12 BORDER.
                                                                      000 6300
                                                                      000 6400
                                                                      000 6500
IP(14+42) IS GAP OF VDD LINE TO CROSS GROUND PAD.
                                                                      000 6600
IP(14+43) DISTANCE FROM EDGE OF CELL ROW TO VERTICAL
                                                                      000 6700
            CHANNEL FOR GROUND.
                                                                      000 6800
IP(14+44) IS SCALE OF CHIP.
                                                                      000 6900
IP(14+45) IS SCALE FOR ALL LINE SETS.
                                                                      000 7000
IP(14+46) IS SCALE FOR ALL SHAPE SETS.
                                                                      000 7100
IP(14+47) IS APERTURE NUMBER FOR LEVEL 2 P LINES
IP(14+48) IS APERTURE NUMBER FOR LEVEL 3 N+ (NOT) LINES
IP(14+49) IS APERTURE NUMBER FOR LEVEL 6 METAL LINES
                                                                      000 7200
                                                                      000 7300
                                                                      000 7400
IP(14+50) IS APERTURE NUMBER FOR LEVEL 6 GROUND LINES.
                                                                      000 7500
IP(I4+51) IS 1/2 X STEP & REPEAT + X-OFFSET.
                                                                      000 7600
IP(14+52) IS 1/2 Y STEP & REPEAT + Y-OFFSET.
IP(14+53) IS ARTWORK CODE FOR COMPONENTS (MUST=100).
                                                                      000
                                                                          7700
                                                                      000 7800
IP(14+54) IS ARTWORK CODE FOR TUNNELS.
                                                                      000 7900
IP(14+55) IS ARTWORK CODE FOR METAL.
                                                                      000 8000
IP(14+56) IS ARTWORK CODE FOR GROUND. IP(14+57) IS ARTWORK CODE FOR VDD.
                                                                      000 8100
                                                                      000 8200
IP(14+58) IS RADIUS OF TUNNEL APERTURE
                                                                      000 8300
IP(14+59) IS RADIUS OF GROUND LINE APERTURE.
                                                                      000 8400
IP(14+60) IS NUMBER OF COMPONENTS GENERATED.
                                                                      000 8500
IP(14+61) IS NUMBER OF
                           TUNNEL LINES GENERATED.
                                                                      000 8600
IP(14+62) IS NUMBER OF METAL-LINES GENERATED.
                                                                      000 8700
IP(14+63) IS NUMBER OF GROUND LINES GENERATED.
                                                                      000 8800
IP(14+64) IS NUMBER OF VDD LINES GENERATED.
                                                                      000 8900
IP(14+65)
                                                                      000 9000
IP(I4+66)
                                                                      000 9100
IP(14+67) IS Y-DIST OF CELL VDD FROM CELL ORIGIN.
                                                                      000 9200
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IP(14+68) IS APERTURE NUMBER FOR LEVEL 6 VDD LINES.
                                                                 000 9300
IP(14+69) IS Y-DISTANCE FROM CENTER OF STREET TO CENTER
                                                                 000 9400
           OF PHOTO KEY, KEY OUTSIDE CHIP.
                                                                 000 9500
IP(14+70) IS DISTANCE OF VDD LINE END POINT FROM GROUND
                                                                 000 9600
                                                                 000 9700
            PATTERN ORIGIN.
IP(I4+71) IS LEVEL 1 X-OFFSET FOR GROUND SHAPE SET.
                                                                 000 9800
IP(14+72) IS LEVEL 1 Y-OFFSET FOR GROUND SHAPE SET.
                                                                 000 9900
IP(14+73) IS LEVEL 2 X-OFFSET FOR GROUND SHAPE SET. IP(14+74) IS LEVEL 2 Y-OFFSET FOR GROUND SHAPE SET. IP(14+75) IS LEVEL 3 X-OFFSET FOR GROUND SHAPE SET.
                                                                 000 0000
                                                                 000 0100
                                                                 000 0200
IP(14+76) IS LEVEL 3 Y-OFFSET FOR GROUND SHAPE SET.
                                                                 000 0300
IP(14+77) IS LEVEL 4 X-OFFSET FOR GROUND SHAPE SET.
                                                                 000 0400
IP(14+78) IS LEVEL 4 Y-OFFSET FOR GROUND SHAPE SET.
IP(14+79) IS LEVEL 5 X-OFFSET FOR GROUND SHAPE SET.
IP(14+80) IS LEVEL 5 Y-OFFSET FOR GROUND SHAPE SET.
                                                                 000 0500
                                                                 000 0600
IP(I4+81) IS CELL-TO-CELL SPACING CONSTANT. CELL
                                                                 000 0800
           DESIGN DEPENDENT (CELL-TO-CELL OVERLAP).
                                                                 000 0900
IP(14+82) IS LEFT SIDE END-CAP(CELL DESIGN DEPENDENT)
                                                                000 1000
                                                                 000 1100
           SPACING CONSTANT. FUNCTION OF
                                                                 000 1200
000 1300
           CELL-TO-CELL OVERLAP.
IP(14+83) IS X-DIST OF SIDE GROUND LINE FROM CENTER
           OF STREET.
                                                                 000 1400
IP(14+84) IS Y-DIST OF BOTTOM/TOP GROUND LINE FROM CENTER OF STREET.
                                                                 000 1500
000 1600
IP(14+85) IS X-DIST OF SIDE VDD LINE FROM CENTER OF
                                                                 000 1700
           STREET.
                                                                 000 1800
IP(I4+86) IS Y-DIST OF BOTTOM/TOP VDD LINE FROM
                                                                 000 1900
           CENTER OF STREET.
                                                                 000 2000
IP(14+87) IS X-DIST OF SIDE B.P. ORIGIN FROM CENTER
                                                                 000 2100
           OF STREET.
                                                                 000 2200
IP(I4+88) IS Y-DIST OF BOTTOM/TOP B.P. ORIGIN FROM
                                                                 000 2300
           CENTER OF STREET.
                                                                 000 2400
           IS HALF THE WIDTH OF LEVEL 8 BORDER.
IP(14+89)
                                                                 000 2500
IP(14+90) IS APERATURE NUMBER FOR SYMBOL DATA.
                                                                 000 2600
IP(I4+91) IS STEP & REPEAT INCREMENT
                                                                 000 2700
IP(14+92) IS BYPASS FOR CHIP KEYS.
                                                                 000 2800
           = 0, GENERATE ALL.
                                                                 000 2900
                                                                000 3000
000 3100
           = 1, BYPASS IP(15+16).
           = 2, BYPASS 15 AND 16.
           = 3, BYPASS 14, 15 & 16.
                                                                 000 3200
           = 4, BYPASS 13, 14, 15, & 16.
                                                                 000 3300
IP(14+93) IS ATL PATTERN BYPASS.
                                                                 000 3400
           = 0, GENERATE
                                                                 000 3500
           = 1, BYPASS.
                                                                 000 3600
IP(I4+94) IS RTCA1 CONTROL.
                                                                 000 3700
           = 0, SET FOR MINIMUM Y-ROUTE.
                                                                 000 3800
           = 1, SET FOR MINIMUM CROSS ON SIDE ROUTE.
                                                                 000 3900
IP(I4+95) IS PRINT CONTROL OF "PICTURE" OF CHIP.
                                                                 000 4000
           = 0, PRINT
                                                                 000 4100
           = 1, BYPASS
                                                                 000 4200
IP(14+96) IS BYPASS CONTROL OF REROUTE OF SIDE 8.P.
                                                                000 4300
           IN POS1.
                                                                 000 4400
           = 0, BYPASS.
                                                                 000 4500
                EXECUTE.
                                                                 000 4600
           = 1
IP(14+97) IS CONTROL FOR EXECUTION OF TONGUE-IN-GROOVE
                                                                000 4700
           FIT OF FACING CENTER ROWS.
                                                                000 4800
           = 0, EXECUTE MINIMUM Y-DIST. BETWEEN ROWS.
                                                                000 4900
           = 1, BYPASS THIS FEATURE.
                                                                000 5000
IP(I4+98) IS CONTROL FOR GENERATING WIDE TUNNEL SEGMENTS 000 5100
           = 0, BYPASS.
                                                                000 5200
           > 0, WIDEN SIDE OF TUNNEL BY RADIUS, IP(14+58),000 5300
```

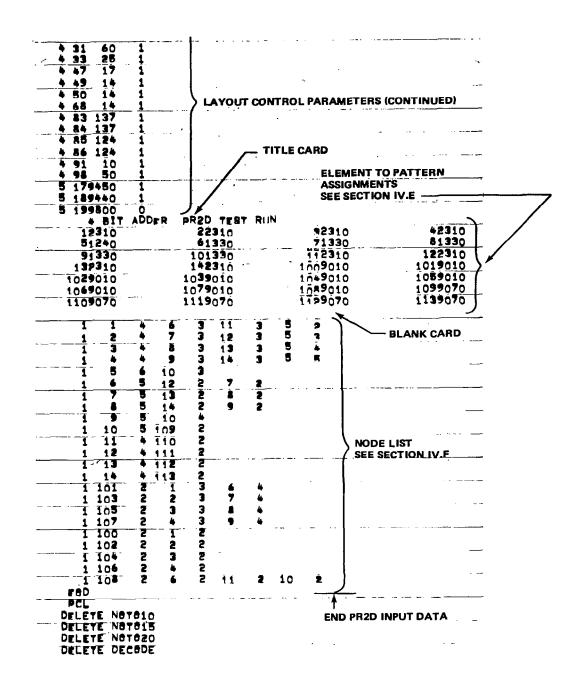
```
OF TUNNEL APERTURE, WHEN TUNNEL SEGMENT
                                                                                 000 5400
                                  IS IP(14+98) UNITS LONG OR LONGER.
                                                                                 000 5500
                 IP(14+99) IS LOW PROFILE METAL ON ROW FOR POSSIBLE
                                                                                 000 5700
000 5800
                            REDUCTION OF TUNNEL LENGTHS.
                            = 0, BYPASS
                             O, MOVE METAL, IP(14+99) INDICIES OR
                                                                                 000 5900
                                 WIDER, CLOSER TO CELL ROW.
                                                                                 000 6000
                              = 0, SET IP(I0+57) = 1 AND IP(I0+70) = 1.
                                                                                 000 61.00
                                                                                 000 6200
           IS IS THE STARTING INDEX FOR PATTERN DATA PARAMETERS.
                                                                                 000 6300
                                                                                 000 6400
                 IP(15
                           ) IS NUMBER OF 15 DATA.
                                                                                 000 6500
                                                                                000 6600
000 6700
000 6800
                 IP(15+
                         1) IS THE BASIC INPUT PAD PATTERN NUMBER
                         2) IS THE INPUT PAD PATTERN # FOR SIDE ROWS.
3) IS BONDING PAD PATTERN NO. LOWER LIMIT.
4) IS LOWER LIMIT FOR OUTPUT BONDING PADS.
                 IP(15+
                 IP(I5+
                                                                                 000 6900
                 IP(15+
                          5) IS PATTERN NUMBER FOR VDD SUBSTRATE TAP.
                                                                                 000 7000
000 7100
                 IP(IS+
                          6) IS PATTERN NUMBER FOR TUNNEL END
                 IP(15+
                          7) IS PATTERN NUMBER FOR PHOTO ALIGNMENT KEY
                                                                                000 7200
000 7300
000 7400
C
                 IP(15+
C
                          8) IS PATTERN NUMBER FOR END CAP
                 IP(15+
                            IF = 0, BYPASS END CAP GENERATION.
                                                                                 000 7500
                 IP(15+
                          9) IS PATTERN NUMBER FOR GROUND B.P.
                                                                                 000 7600
000 7700
000 7800
                 IP(15+ 10) IS PATTERN NUMBER FOR VDD B.P.
                 IP(15+ 11) IS PATTERN NUMBER FOR P-TEST TRANSISTOR
                 IP(15+ 12) IS PATTERN NUMBER FOR N-TEST TRANSISTOR
                 IP(15+ 13) IS PATTERN NUMBER FOR CHIP ALIGNMENT KEY # 1
                                                                                 000 7900
Č
                 IP(IS+ 14) IS PATTERN NUMBER FOR CHIP ALIGNMENT KEY # 2
                                                                                 000 8000
                 IP(15+ 15) IS PATTERN NUMBER FOR CHIP ALIGNMENT KEY # 3
                                                                                 000 8100
                 IP(15+ 16) IS PATTERN NUMBER FOR CHIP ALIGNMENT KEY # 4
                                                                                 000 8200
                 IP(I5+ 17) IS PATTERN NUMBER FOR RCA
                                                                                 000 8300
                 IP(I5+ 18) IS PATTERN NUMBER FOR ATL
                                                                                 000 8400
C
                 IP(15+ 19) IS PATTERN NUMBER FOR BASIC ID DATA
                                                                                 000 8500
                                                                                 000 8600
           16 IS STARTING INDEX FOR CAPACITIVE PARMETERS.
C
                                                                                 000 8700
                                                                                 000 8800
                 IP(16 ) IS NUMBER OF 16 DATA.
IP(16+ 1) IS CAPACITANCE PER LINEAR 10TH MIL OF METAL.
                                                                                 000 8900
000 9000
                 IP(16+ 2) IS CAPACITANCE PER LINEAR 10TH MIL OF TUNNEL.
                                                                                 000 9100
                 IP(16+ 3) IS 1/2 CAPACITANCE OF A TUNNEL END.
                                                                                 000 9200
                 IP(16+ 4) IS TOTAL LENGTH OF METAL LINES (MAXIMUM = MAX) 000 9300
С
                 IP(16+ 5) IS OVERFLOW OF IP(16+4) = # OF MAX
                                                                                 000 9400
                 IP(16+ 6) IS TOTAL LENGTH OF TUNNEL LINES (MAXIMUM = MAX)000 9500
                 IP(16+7) IS OVERFLOW OF IP(16+6) = \# OF MAX
                                                                                 000 9600
C
                 IP(I6+ 8) IS TUNNEL END COUNT
                                                                                 000 9700
C
                                                                                 000 9800
                                                                                 000 9900
       IPIN(IX, IY):
           THIS ARRAY IS USED TO STORE PIN DATA FOR EACH PATTERN USED
                                                                                 000 0000
           IN THE DESIGN OF THE CHIP. IX IS A RUNNING INDEX. PIN DATA
                                                                                 000 0100
           ARE STORED ONE AFTER ANOTHER. THE LAST PIN OF THE PATTERN 000 0200 IS A NON-ACTIVE PIN AND SPECIFIES THE X/Y SPAN OF THE PATTERN 000 0300
Ċ
           WITH RESPECT TO THE PATTERN ORIGIN. THE REFERENCE PIN (NON-
                                                                                 000 0400
           ACTIVE) MUST HAVE A PIN REASSIGNMENT OF ZERO. THE FORMAT OF
                                                                                 000 0500
           THE PIN DATA READ FROM CARDS OR TAPE IS (514,10A4,14). THE
                                                                                 000 0600
                                                                                 000 0700
000 0800
           DATA READ IN ARE: PATTERN NO., PIN NUMBER, PIN REASSIGNMENT
           FLAG, PIN X-COORDINATE FROM PATTERN ORIGIN, PIN Y-COORDINATE
           FROM PATTERN ORIGIN/ PIN CAPACITANCE, PATTERN DESCRIPTION AND 000 0900
Ţ
           TECHNOLOGY CODE.
                                                                                 000 1000
C
                                                                                 000 1100
           FOR THE I PATTERN, STORAGE STARTING AT POSITION IX:
                                                                                 000 1200
                 IPIN(IX,1) IS PATTERN NUMBER FOR I PATTERN.
С
                                                                                 000 1300
                 IPIN(IX,2) IS # OF PINS IN PATTERN I, ACTIVE PINS + T.
                                                                                 000 1400
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IPIN(IX,3) INDEX LOCATION OF START OF I+1 PATTERN.
                                                                               000 1500
Ç
                 IPIN(IX+J,1) REASSIGNMENT FLAG FOR PIN J.
                                                                               000 1600
FOR REF. PIN MAY BE USED TO INDICATE NUMBER
                                                                               000 1700
                                                                               000 1800
                             OF DEVICES ON CELL = - # DEVICES.
                                                                               000 1900
000 2000
                       IPINYR = 1
                 IPIN(IX+J,2) X-COORDINATE OF PIN J FROM PATTERN ORIGIN.
                      IPINYX = 2
                                                                               000 2100
                 IPIN(IX+J,3) IMPEDANCE OF PIN J / REF. PIN Y-COORD.
                                                                               000 2200
                                                                               000 2300
                      FOR IMPEDANCE:
                      FIRST TWO DIGITS ARE PIN RESISTANCE IN X.X K-OHMS.
                                                                               000 2400
                      SECOND TWO DIGITS ARE PIN CAPACITANCE IN .XX PICO-F.000 2500
                      IPINYC = 3
                                                                               000 2600
                      IPINYY = 3
                                                                               000 2700
                                                                               000 2800
                                                                               000 2900
       LIM(IX):
           LIM(IX) IS USED TO STORE PROGRAM CONSTANTS. SEE EQUIVALENCE
                                                                               000 3000
                                                                               000 3100
0000,000
           STATEMENT.
                                                                               000 3200
                                                                               000 3300
       MODE(IX):
                                                                               000 3400
           SEE EXEC.
                                                                               000 3500
                                                                               000 3600
       NODE (IX):
                                                                               000 3700
           NODE(IX) CONTAINS NODE DATA AS WELL AS ELEMENT-PIN CONNECT-
                                                                               000 3800
00000
           IVITY AMONG DEVICES FOR THE NODE. ALL NODE DATA ARE STACKED
                                                                               000 3900
           ONE AFTER THE OTHER IN THE NODE(IX) ARRAY.
AS AN EXAMPLE, SAY THE J NODE HAS A STARTING ADDRESS OF K
                                                                               000 4000
                                                                               000 4100
           IN ARRAY NODE(IX). THEN:
                                                                               000 4200
                          CONTAINS THE "OLD" LENGTH OF NODE J.
C
                                                                               000 4300
                 NODE(K)
                 NODE(K+1) CONTAINS THE "NEW" LENGTH OF NODE J. NODE(K+2) CONTAINS THE ADDRESS OF THE LAST
                                                                               000 4400
C
                                                                               000 4500
                            ELEMENT OF NODE J. SAY THIS IS LOCATION L.
                                                                               000 4600
NODE(K+3) CONTAINS THE WEIGHTING FACTOR FOR NODE J.
                                                                               000 4700
                 NODE(K+4) THROUGH NODE(L+1) CONTAINS THOSE ELEMENT-PIN 000 4800 CONNECTIVITY DATA (IN ELEM-PIN PAIRS) OF NODE J000 4900
                 NODE(L+2) IS THE STARTING ADDRESS OF NODE J+1.
                                                                               000 5000
                                                                               000 5100
       **********************
                                                                               000 5300
                                                                               000 5400
           IN ORDER TO CHANGE THE UPPER LIMITS OF ANY ARRAY IN THIS
                                                                               000 5500
000000000
           PROGRAM, EACH AFFECTED DIMENSION STATEMENT MUST BE
                                                                               000 5600
           CHANGED AND THE AFFECTED UPPER LIMIT VARIABLES MUST BE
                                                                               000 5700
           CHANGED. IN ORDER TO HELP MAKE THESE CHANGES, COMMENT CARDS HAVE BEEN WRITTEN AT STATEMENTS SETTING UPPER LIMITS
                                                                               000 5800
                                                                               000 5900
           OF ARRAYS. THE FORM OF THESE COMMENTS ARE (FOR EXAMPLE):
                                                                               000 6000
                                                                               000 6100
                                       POSSIBLE OTHER
                                                             UPPER
                                                                       ARRAY
                                                                               000 6200
           POSSIBLE OTHER
                                       ARRAYS AFFECTED:
                                                             LIMIT:
                                                                       NAME:
                                                                               000 6300
           LIMITS AFFECTED
                                                                               000 6400
                                                            MAPX ----
                                                                       MAP
                                                                               000 6500
C
                                                                               000 6600
C
        ***************
                                                                              *000 6700
C*
                                                                               000 6800
Ç
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## VI. PR2D EXAMPLE DESIGN LAYOUTS

A. N002 - 4-Bit Adder

```
23120 MAY 107178 ID=0088
JBB MARTIN, EC45020 (BAST/CAD), 1
 LIMIT (TIME, 18), (CORE, 60)
 ARSIGN F16, (DEVICE, LB)
 ASSIGN Fil, (FILE, DECODE), (OUTIN), (GAVE)
 ASSIGN F15, (DFVICE, CR)
 ASSIGN F110, (FILE, NOTO 10), (OUTIN), (RAVE)
 ASSIGN F115, (FILE, NOTO15), (OUTIN), (RAVE)
ASSIGN F120, (FILE, NOTO20), (OUTIN), (RAVE)
 ASSIGN F:25, (FILE, NOTO25), (OUTIN), (RAVE)
 ASSIGN F130, (FILE, DESIGN), (IN)
 ASSIGN F135, (FILE, COMON1), (OUT), (SAVE)
ASSIGN F140, (FILE, COMON2), (IN)
ASSIGN F145, (FILE, SOSSWPDF, GAULD), (IN)
 ASSIGN FISO, (FILE, SOSBWPDFM), (BUT) T, SAVE)
 ASSIGN F155, (FILE, AWRK1X), (OUTIN), (RAVE)
 ASSIGN FIGO, (FILE, AWRKPC), (GUTIN), (SAVE)
 PCL
  COPY NOOR TO LP
  JOB MARTIN, EC45020 (BAST/CAD), 1
  LIMIT (TIME, 15), (CORE, 60)
  ASSIGN F16, (DEVICE, LG)
  ASSIGN Fig. (FILE, DECODE), (OUTIN), (RAVE)
  ASSIGN FIS, (DEVICE, CR)
  ASSIGN FILO, (FILE, NOTO10), (AUTIN) TISAVET
  ASSIGN F115, (FILE, NOTO15), (AUTIN), SAVE)
  ASSIGN F120, (FILE, NOTO20), (AUTIN); (SAVE)
  ARSIGN F125, (FILE, NOTO25), (AUTIN) 7, SAVE)
  ARSIGN FIBO, (FILE, DESIGN), (IN)
  ASSIGN F135, (FILE, COMON1), (BUT), (BAVE)
"ASSIGN F140, (FILE, COMONE), (IN)
  ASSIGN F145, (FILE, SOSBWPDF, GOULD) ... (IN)
 ASSIGN F150, (FILE, SOSRWPDFM), (OUT) (SAVE)
ASSIGN F155, (FILE, AWRK1X), (AUTIN) (SAVE)
ASSIGN F160, (FILE, AWRK2C), (AUTIN) (SAVE)
 PCL
 COPY NOOF TO LP
 FND
                                                      BEGIN PR2D INPUT DATA
 RUN (LMN, LMPR2DC, GOULD)
 DATA
 1-1 4-4
             02
                    5
                          1 101
                                    1
                                          ٥
       50
0
               1
    1
7
       105
                                                               MODE CARD
2
    5
        62
               1
                              - IP(10+1) = 50
                                                                SEE SECTION IV.A
2
  13
        12
               1
      300
    1
               1
    5
      300
               1
    3
        25
               1
    9
      500
                         LAYOUT CONTROL PARAMETERS
  25
      105
                          SEE SECTION IV.A (18) AND SECTION V.
      __1
4 26
               1
                          FOR FORMAT AND EFECTS OF EACH PARAMETER.
4 27
         0
               1
<del>* 30</del>
       25
```



DELETE AWRKEC
END
END
END
END
PCL PROCESSING TERMINATED

RUN (LMN.LMPR.DC.GBULD)

	T-	START OF INITE
TWO-DIM	TENSIONAL PLACEMENT & ROUTING OF LSI CHIPS.	
	WRITTEN BYS RICHARD NOTO	
	COMPANY : REA CORP ( ADDRESS : FRANT & GOOPER STS.	
	CAMDEN, No Lo 08102	
	PHONE NO. 1 (409) 963-8000 EXT. NO. 1 PG 6755	
<del></del>		PR2D REVISION B, FEB. 1974.
	IP(10+ 1) = 50	
	IP(I2+ 4) 9 102	
	IP(12+ 5) = 42	
	IP(I2+ i3) + 12	
	IP(I4+ 1) = 300	
	IP(T4+ 2) + 300	
	IP(I4+ 3) 4 25	· · · · · · · · · · · · · · · · · · ·
	IP(I4+ 9) # 500	- ·
	IF(14+ 95) = 105	
	IP(I4+ 96) • 1	<u></u>
	IP(I4+ 27) = 0	·· ·· ·
	TF(14+ 30) = 25	
~ <del></del>	IP(I4+ 31) # 60	
	•	
	1P(14+ 33) # 25	
	IP(14+ 47) # 17	
	IP(14+ 49) = 14	· · · · · · · · · · · · · · · · · · ·
	IP(I4+ 50) e 14	
	TP(144 A8) # 14	
	TP(14+ 83) # 137	

	END OF INITY START OF INPUT FUNCTION:
10(15+ (9):e +800	
IP(18+ 18) a 9440	
1P(15+ 17) a 9480	<u> </u>
1P(14+ 98) = 50	
1P(14+ 91) 0 10	·
IP(144 86) 0 184	<u></u>
IP(14+ 85) s 124	

n L

4 BIT ADDER	DATA FOR PATTERNS USED ON CHIP:										
PIN DATA FOR PATTERNS	USED	ON CHIP	<u></u>								
PATTERN 12	4ô	<b>#</b> Î									
	RESN FLAG	X nist	PIN IMPa								
1	0	0	0								
2		5 15	0								
	Ĭ	25 35	<u>0</u>								
	10	45	0								
	-8	<b>5</b> 0_	<u>70</u>								
PATTERN 13	3ñ										
PIN No.	RESN FLAG	nist	PIN IMP.								
1	0	0	0								
2	1	15	0								
	3	35	0		<del></del>						
	10	45 50	70								
PATTERN 23	 Ln	·		<del></del>							
PIN	RESN	×	PIN								
No.	FLAG	<b>nist</b>	IMP.	,	<del></del>	<del></del>					
	- 0	0	-0								
·	<del>1</del> -	<u>25</u> 35	0			• •					
	10	45	0								
5	-8	60	70								

## 4 BIT ADDER PR2D TERT RUN PIN DATA FOR PATTERNS USED ON CHIP!

PATTERN	901	ń	<b>#</b> *	
•	PIN.	RESN Flag	X ništ	PIN Imp.
	1 2 3	10 0	0 10 80	0 0 <b>9</b> 0
PATTERN	907	ö .	• i	
	PIN No.	RESN Flag	X nist	PIN Imp.
	1 2 3	0 10 •2	0 20 80	0 0 90

## 4 BIT ADDER PRED TERT RUN

PAGE No. 3

<u>tl</u> em	PTRN	erD.	RaW	ELEM	PTRN	ero.	Rê₩	•••							
1	2310	0		,	2310	0	0	3	2310	0	. 0	. 4	2310	0	
	1240	o	0		1330	0_	0	7	1330	0	0	8	1330	0	
9	1330	0	0	1 ñ	1330	0	0	11	2310	Õ	0	12	2310	<u>.</u> 0	
13	2310	0	0	14	2310	0	0	15	U	.0	•	16	. 0	. 0	
1,7	. 0	Ò	0	18	0_	0	D	19	<u>U</u>	<u> </u>	0	20	0	0	
21	0	0	O	25	0	0	0	53	Ų	0	Ö	24	. 0	Ö	
25	0	0	0	. 24	0	0	.0	27	Ų	Ó	. 0	58		0_	
29		0	0	30	0	<u>0</u>	0	31	U	0_	0	32		0	
33	. 0	0	0	34	0	0	٥.	35		0	<u>·</u> •	36	0	0	
37	3	0	0	JÁ	0	0	٥	39	. · · · · · _	0	0	<b>+</b> 0		0	
41	0	0	0	49	0_	0	0	43		0	0	44	0	0	
45	, 0	0	0	44	0	0	Ō	. 47	_ · · ·	. 0	. 👱 .	4.8	· 0		
4.9	0	0	0	Sń	0	• ,	_ 0	51	·	<u>0</u> .		52		O	
53	0	0	0	54	0	0	0	55	U	0_	0	56	0.	0	
57	0	. 0	0	58	0	. 0	. 0	59		0	0	_60_	0	0	
41	0	0	. 0	69	٥	0	0	_63	<u> </u>	_0_	0	44	0	0	
45	0	0	0	64	0	0_	0	67	U	0	0	68	0	0	
	0	0	0	7ñ	0	0	٥	71		_ 0_		72		0	
73		0	0	7.	٥	0	Ō.	75			0	76	0	0	_
77	0		0_	78		0	0	79	U	0	0	80	0	0	
81	0	. 0	0	82	0	0_	0_	83	<u> </u>	0	0	84	0	0	
15	0	0	٥	84	0	0	0	87	U	0	0	88	0	0	

	, (	BIT	ADDER	PR	2D T#	RT RUN							PAĢE	Ne.	4	
ELEM	ELFM	PATT	ERN AS	SS (GI	ELEM	PTRN	eRĐ,	ROW	•••		<del></del>			· ~ ···		
	<b>A9</b>	. 0	0	0	9ñ	0	0	0	. 91	ñ	٥.	0	92	0	0	.0
	13	_ o	0	0	94		_0	_,, Q	95		0	0	96	0	<u> 0</u>	0
	_ 97	. 0	0	0	98	0	0.	0	99	Õ	_ 0.	٥	100	9010	0	o
	161	9010	0	0	109	9010	ο.	0	103	9010	Ο.	0	104	9010	0	.0
	105	9010	_	.0	104	9010	0.	Õ	107	9010	0	_ 0_	108	9010	0	. 0
	109	9070	0	0	116	9070	0	0	111	9070	<u>o</u>	٥	112	9070	0	0
	113	9070	0	٥					_							•

4	BIT	ADDER	PKE	D TEST	RUN					PAGE NO.	5
NODE LISTS NODE NO.	NODE WT.	ELEM	ρĵΝ	EĽĖM"	PIN	ELEM	PIN	ELĖM	PIN	• • · · · · · · · · · · · · · · · · · ·	
- · 2	1 1 1	1 2 3 4 5	4 4 4 6	4 7 R 9	3 3 3 3	11 12 13 14	3 3 3	5 5 5 5	2 3 4		
6 7 8 9	1 1 1 1	6 7 8 9	55555	10.2 10.2 10.0 10.0	3 2 2 2 4 2	7 8 9	5 5 5				
11 12 13 14 15 16	1 1 1 1 1	11 12 13 14 101 103 105	+ + + + 2 2 2	1 1 7 1 1 7 1 1 7 1 7 1 7 1 7 1 7 1 7 1	2 2 3 3	6 7 8	4			· · · · · · · · · · · · · · · · · · ·	,
18 19 20 21 22 23	[	107 100 102 104 106 108	32535	4 6	3322222	11	2	10			

NODE ERRORS!

END OF INPUT FUNCTION.

65

4 BIT ADDER PRED TERT RUN

PAGE NO.

START OF PRPL1 SUBROUTINE:

END OF PRPL1 SUBROUTINES START OF PLACEMENT!

	4 BIT ADDER BRED THAT RUN													PAGE NO.			7			
CENSTHO	12,		INT	•	71	JR	<b>1</b> P+2,	TO.	TAL	DIST		0*	# 0000	INT+=	0,		EQU	INT	•	71
PLACEMENT																				
<u>o</u> .	. 0	0	107	0	108	0	ĩ bạ	0	104	0	0									
0 0 0 102	0	0	o	0	o	0	0	ŭ	Ö	. 6	Ö.	103								
102	0	0	11	12	13	1 4	10	9	1	. 0	0	105								
110	ö	ŏ	0	C	ō	0	ō	Ų	ō	ŏ	Ö	106								
101	. <u>0</u>	0	111	0	112	0	113	Ü	100	00000000	0	0								
TOTAL WIR	E UN	IŦ	BY	NBD	F 8															
ō.	0			0		0 0		0		0		0	· o	0		0				
Ģ.	0	1		0		Q		0		O		0	ō	0		0				

Training to the second

	4	91	IT A	DDER	PR2	D T	FST R	ŲN						PA	GE NO	ı	8		
LENGTH	61	#	INT		128	اجال و	EP=2,	70	TAL	DIST .	,	0,	# G00D	INT.=	5, 4	EUU	INT	•	118
PLACEMENTS																			
. 0	C	0	108	•	109	c	104	U	105	с	0	0							
0 0 0 101	0 0 0	0	Ü	0	0	2	C	Ü	0	0	0	103							
101	0	0	12	10	9	7	C	14	0		0	106							
0	Ö	0	6	13	11	ė	9	Î.	7	. 0	ŏ	100							
111	0	0	C	0	0	0	c	Ü	0	Ċ	0	107							
110	0	0		0	. 0	0	C	0	0	9	0	0							
0	n	0	112	C	11.5	ā	100	U	104	С	0	0							
manul valo																			
TOTAL WIR	E UN	IŢŞ	<b>∌</b> 8∀	וניטא	7 1														
0	0			C		0	4	0		0		0	0	0	0				
Ō						)	1	0		0		9	0	0	0				
0	0			С															
TOTAL WIR		175	\$ BY				Ŧ00		102	00	ō	000	0	o 0	0				

# RIT ADDER PR2D TEST RUN PAGE NO. 9

LENGTH: 1; # INT. = 244, URSE: 40 TO TO TIST. = 402 # 300D INT. = 29, # EQU INT = 152

PLACEMENT:

TOTAL WIRE UNITS BY NODEL

ς:

ţ

3	5	7	8	4	8	4	4	1	24
<b>1 B</b>	24	26	38 38	8	10	18	24	18	54
24	24	78							

2 50 2 45 54 667 55 52 56 1031 57 107 58 177 45 125 46 51 51 46 52 46 1 1 1 4 45 

4 BIT ADDER PRZD TÉŘT RUN

PAGE NO. 10

LENGTHO D. # INT. + 401, JREPOA, TOTAL DIST. 627, # 6000 INT. 29, # EQU INT . 159

PLACEMENTS

TOTAL WIRE UNITS BY NODE:

10 6 14 15 11 15 5 11 2 36 36 38 24 52 46 24 32 38 8 38 92

ACTUAL WIRE UNITS . 143 143

TOTAL WIRE UNITS BY NODE!

5 6 9 10 6 10 5 6 2 3 3 4 7 6 8 7 11 9 4 3

END OF PLACEMENT.

4 BIT ADDER PRED TEST RUN

PAGE NO. 11

START OF FIX1 SUBROUTINE:
END OF FIX1 SUBROUTINE.

PAGE NO. 12 START OF POST-PLACE SUBROUTINE:

	Y D	IME	N S I C	۰	->								
R	R	R	R	Q	R	ĸ	R	R	н	R	н	ĸ	
•	P	0	•	•	•	۵	8	•	•	Ħ		8	
W	W	*	K	W	w	W		4	W	W	m	W	
•	•	*		ø		*	#	#	*	*		*	
3	•	5	6	7	8	y	10	11	12	13	14	15	

## Rew # 11

VDD	101	112	1 ñg
0	1	11	110
	4	5	
•	1 ñ	13	111
	•	12	
6	•	2	102
	1		
•	14	7	103

767 119 105 gnn

END OF POST-PLACE SUBROUTINE.

4 BIT ADDER PRED TEST RUN

PAGE NO. 13
START OF ROUTE FUNCTION:
START SMASH1:

ELEMENT NUMBER	ROW / ORIFN'		MY2 BIN S	E-ASSIGNMENT Changed to	
•					
<b>2</b>	•5		3	3 2	
5	•5		2 5	. <u>5</u>	
7	-5				
9	-4		-		
12	-5				
13	-5				
		START OF	CENT	ER ROUTING:	END SMASHI
				ROUTING.	
		START OF	SIDE	ROUTING	

4 BIT ADDER BRED THAT RUN

PAGE NO. 15 START OF ART FUNCTION:

77

4 BIT ADDER PRED THAT RUN

PAGE NO 16

START ANALCI END ANALCI: 4 BIT ADDER PRED TERT RUN

PAGE NO. 17

START ANALS1 END ANALS1. 4 BIT ADDER PRZD TPÄT RUN

PAGE NO. 18

START PWRI END PWRI 4 BIT ADDER PRED THET RUN

PAGE NO. 19

START SORT1 END SORT1. 4 BIT ADDER PRED TEST RUN

PAGE NO. 20

START ARTS

4 BIT	ADDER P	R2D TES	RUN PAGE NO.	21
ARTWORK INSTRUC	TION DAT	rA1		
09250	400	-115	0	1 2 3 4 5 6 7 8 9
09800	0	0	0	~
09800	20	0	0	3
09820	40	0	Ö.	
09440	Ō	35	100	4
29010	307	101	109	7
29070	387	101	106	Ŕ
59010	423	101	104	Ğ
59010	503	101	104	10
09210	695	105 144	21	11
09500	495 575	151	14	12
09500	225	158	19	13
09500	435	158	18	14
09500	935 235	155 165	15	14 15 16
09500 09500	295	165	15	16
09500	305	165	15 6	17
0950 <u>0</u>	385	165	55	18
09500	565	165	4	19 20
79110	101	172	0	50
09500	355	172	10	21
09500	405	172	<b>4</b>	55
09500	515	172	3	21 22 23
19010	699	172	107	24
09500	265	179	23	25
09500	395	179	18 18	26 27
09500	435	179	18	27
09500	555	179	8	28
09500	245	186	1	29
09500	275	186	1	30
09500	315	186	23	31 32
09500	325	186	5	32
09500	345	186	9	33
09500	425	186	9	34
09500	435	186	18	35 36 37
09800	455	186	<u>*</u>	36
09500	465	186		37
09500	505	186	17	38
09310	200	193	1	39
01330	260	193	.6	39 40 41 42 43 44
01330	310	193	10	*1
02310	360	193	<u>*</u>	42
41330	470	193	9	43
02310	470	193	3	44
02310	530	193	14 101	45 46
79010		252		

19070	699	252	113	47
09500	169	256	5	48
09500	179	256	23	49
09500	189	256	1	50
09500	602	256	17	51
09500	612	256	8	52
04500	622	256	3	53
09500	632	256	•	54
09800	642	256	6	55
09500	169	270	5	56
09500	179	270	23	57

4 B:	TT ADDER P	RED TERT	RUN	PA	GE NO	55
ARTWORK INSTR	RUCTION DAT	·A1				
09500	189	270			1	58
09500	602	270			17	59
09500	612	270			8	60
09500	622	270			3	61
09500	632	270			4	62
09500	642	270			6	63
<b>39</b> 070	101	288			112	64
69010	699	288			105	65
79320	91	290			0	66
14310	709	<b>29</b> 0			0	67
_09 <b>5</b> 00	149	319			13	68
09500	162	326			23	69
09500	179	326			23	70
09500	605	326			17	71
09800	649	326			17	72
09500	149	333			13	73
59310	200	333			11 5	74
21240	310	333			5	75
22310	370	333			13	76
29310	430	333			12	77
22310	490	333			12 2 8	75
51330	490	333			5	79
23 330	590	333			7	80
-04800-	235 275	340			1	81
09500		340			1 3 2 2	82
09500 09500	285	34 <u>0</u> 340			3	83
04500	335 395	340			3	84 85
09500	445	340			5	
09500	445 445	340			16	86
09500	555	340 340			16	87 88
09500	225	347			10	89
09500	265	347			23 5	<b>9</b> 0
04500	295	347			9	91
09500	495	347			2 7	9ż
_04200	305	347			3	93
09500	525	347			17	94
09500	305	354			4	95
09500	335	354			3	96
09500	345	354			7	97
09500	535	354			á	98
34010	101	368			108	99
09500	245	368			111	100
64100	699	368			ō	101
09500	325	375			13	102
09500	385	375			iż	103
		<del>-</del> -			•-	

09500	405	375	6	104
09500	495	375	ž	105
09500	585	375	6	106
09500	445	385	ž	107
<b>59500</b>	545	382	Ť	108
09500	575	352	2	109
09500	455	389	. 20	110
09500	555	396	16	111
09220	695	435	ŏ	112
49070	307	439	110	113
4 <b>9</b> 070	387	439	ĨĨĪ	114

•	BIT ADDER pR2	D TEST RUN.		PAGE	N8+	23
ARTWORK INS	TRUCTION DATA:					
09010 09010 09250	503	439 439 655		END COMPONENTS	102 103 0	115 116 117 118
o	-196016H51ATL	5.1VL=1.	4 BIT ADDER	SYMBOL PM2D TEST RUN END LEVEL1 SYMBOL		119 121 122
0	-196 <sub>016H51</sub> ATL	2.1 VL-21	4 BIT ADDER	PM2D TEST RUN END LEVEL2 LINE SET		124 125
17 11223344556677788990011112233445566677788899001111223344455666777	10 149 149 169 169 179 189 189 189 189 189 189 189 189 189 18			Light Control of the	3355331199335511553355224466333377	11111111111111111111111111111111111111

18	355	ïa6			10	161
18	355	172			ĩŏ	162
19	385	165			22	163
19	385	186		•	22	164
20	385	340	•		12	165
20	385	375			12	166
21	395	186		· ·	18	167
21	395	179			18	168
22	405	172		•-	- <b>4</b>	169
72	405	186			4	170.
23	405	340	•	•	. 6	17 <u>1</u>
23	405	375			6	172

l i

	4	BIT ADDER	PR2D TERT	RUN		PAGE	N0 •	24
ARTWORK	INS	STRUCTION D	PATAL					
	24	435	179				18	173
	24		158				18	174
	25		340				_5	175
	25		382				2	176
	26	455	389				20	177
	26	455	340				20	178
	27		144				21	179
	27	495	186				21 7 7 3	180
	28		340				7	181
	28		375				7	182
	29		347				3	183
	53		340				3	184
	30		172				3	185
	30		186				3	186
	31		347				17	187
	31	525	340				17	188
	35		340				5	189
	32		354				17 17 8 8 7	190
	33	545	SAE				7	191
	33		340				7	192
	34		179				8	193
	34	555	186				8 16	194
	35 35		340				16	195
	36		396				4	196 197
	36		186 165				4	198
	37		151				14	199
	37		186				14	500
	38		340				17	201
	31		382				2	202
	39		375				14 2 2 6	203
	39		340				6	204
	40		256				17	205
	40		270				17	206
	41		270				s	207
	7.1		256				8	208
	42		256				3	209
	42		270				3	210
	43		270				4	211
	43		256				4	212
	44		256				6	213
	44		270				6	214
	45		326				23	215
	45		396				23	216
	46		326				23 17	217
	46		376				17	218

				END LINE SET	219
٥	-196016H51ATL	2.1 VL-3,	4 BIT ADDER		
•			.,	END LEVELS	222
				SYMBOL	253
0	-196016H51ATL	211 14-41	4 BIT ADDER	PM2D TEST RUN	
•	21125			END LEVELA	225
				SHAPE SET	556
Ã	10				227
1	Ö 269				228
Ž	-50 249				225

	4 8	IT ADDE	R PR2D	TEST RUN					PAGE	`N8 •	25		
ARTWORK	INST	RUCTION	DATAL									٠	
	<b>.</b>	<b>∓</b> 50	590							<del></del>		230	
	•	850	590							٠.	1	231	
	5	850	249									232	
	•	800	243								7	533	
	7	800	540								. 8	234	
		<u>.</u> 0	540									235	
							SHAPE	SET			1	236	
		1	0								1	237	
	1	Ģ	271									238	
	2	-5ò	271									533	
	3	-50	-50									240	
		<b>85</b> 0	-50					-				241	
	5	850	271									242	
	•	<u>8</u> 00	271									243	
	7	800	٥									244	
		n	0								1	245	
•	_			. = = =			SYMBOL				1	246	
	0_	-196016	HE1ATL	271 VL-5,	4 617	ADDER P	KSD TES						
							END LE	VELS				248	
							LINE S	ET .	-			249	
14		10										250	
		127	302							13		251	
	1	127	308							13		252	
	5	127	375							23		253	
	5	127	378							23		254	
	. 1	142	378 326							23		255 256	
		142	305							23			
	<del>-</del>	149								13		257	
	Š	149	3 <u>1</u> 9 333							13 13		25 <b>8</b> 253	
	<del></del>	149	361		:					13		260	
	7	159	242							15		261	
		159	145							4 18		292	
	7	169	172							15		263	
<del></del>	<del></del> -	169	1/4 256							5		264	
	á	169	270							5	9	265	
	<u> </u>	- 169	— <del>35</del> 4							5		266	
	•	179	347							53		267	
	<del>,</del> .	179	270						_	23		268	
	10	179	256							23		249	
	10	179	179							23		270	
	11	189	175							1		271	
	11	189	266			<del></del>				<del></del> -		272	
	12	189	270			•				1		273	
	12	189	340							1		2/3 274	
		<b>447</b>	440										
	13	265	364							5	•	275	

14	287	368	11	277
14	287	413	11	278
15	295	375	13	279
15	295	341	13	280
16	297	127	19	281
16	297	158	19	282
17	305	348	<sup>™</sup> •	283
17	305	354	<b>4</b>	284
18	315	179	23	285
18	315	186	23	286

	4 BIT	ADDER	PR2D	TFeT	RUN	PAGE	N <b>0</b> •	56
ARTWORK	INSTRU	CTION D	ATA					
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	19 :	3,25	172				5	288
			145				6	289
	20	335	179				6	290
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-	21	335	341				3	292
	22	367	413				12	293
	72	367	375				12	294
	23	367	172				10	295
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	24	375	172				6	297
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	25	385	145				22	299
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	4 B1	T ADDER	PRZD	TERT	RUN				PAGE	NO.	27	
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	49	565	151							18		348
	50	575	349							<b>.</b>		349
	50	575	382						•	<u>Ž</u>	-	350
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	51	602	256							17		352
	52	602	270							17		353
	52	602	347							17		354
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	54	612	179							8		358
	55	655	172							3		359
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	57	632	348							4		363
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	58	632	145							4		366
	59	642	158							6		367
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	61	652	298							17		372
	62	652	235							14		373
	62	652	161				 		··	14-		374
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	64	673	235							14		377
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	86	395	179					18	422
	87	375	179					6	423
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	89	189	186					1	427
	89	275	186					1 9 9 17	428
	90	345	186					9	429
	90	425	186					9	430
	91	505	186					17	431
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102	602	347	17	453
102	525	347	17	454
103	495	347	7	455
103	415	347	7	456
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121	295	375		13	492
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122	127	375		23	494
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4 BIT ADDER PRED THAT RUN						PAGE NO. 30			
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	. 8			2-1 VL-7,	4 BIT ADDER	SYMBÖL P <sup>k</sup> 2D Test Run End Levelt		561	

## END ART1.

& BIT ADDER PRED TERT RUN

PAGE NO. 31

CHIP STATISTICS:

X-STEP . ASA Y-GTEP . 59

.63 INCHES OF METAL, .10 INCHES OF TUNNEL, AND 77 TUNNEL E

CELL ROW: THEAR. MILS: TOTAL LINEAR MILS:

4 37.0

5 39.0

75.0

CHIP AREA CELL -11- AREA 5.P. --- AREA WIRING -- AREA WIRING EFFECT SQ. MILS 0/0 SQ

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### CENTER SECTION . 5

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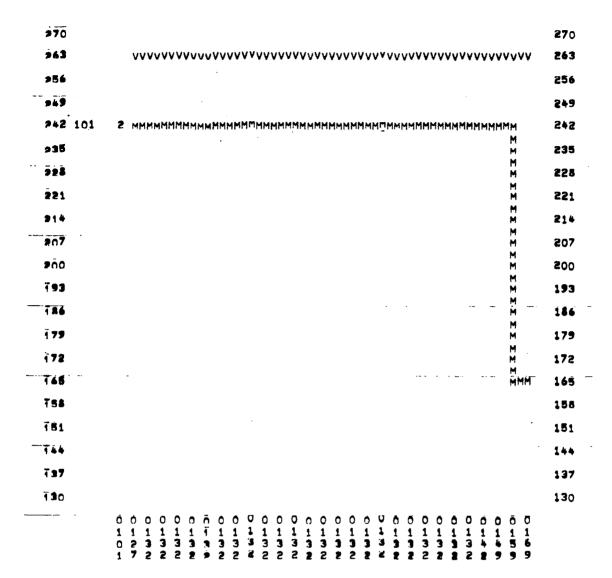
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# 4 BIT ADDER PRZD TPÁT RUN

PAGE NO 41

### NODE CROSSOVER COUNTS

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4 BIT ADDER PRED THET RUN

PAGE NO. 42 START OF WRITE FUNCTION.

### 4 SIT ADDER PRED TERT RUN

### PAGE N8. 43 START OF DEBUG SUBROUTINE.

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USER SERVICE TIME	229
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PAGES! PROCESSOR PAGES	, j
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CORE: PEAK CORE(PAGES)	112
PAGE . MINUTES	619.1638
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FILE SPACE	17504
AVEBL RAD PERMANENT	64
NET DISK PERMANENT	53
AVLBL DISK PERMANENT	
RESOURCES ALLOCATED	1030
C0+120(PAGES)	

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B. C015 — Programmable Timer

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ASSIGN F:5, (DEVICE, SI)
ASSIGN F:10, (FILE, NOTO10), (OUTIN)
ASSIGN F:15, (FILE, NOTO15), (OUTIN)
ASSIGN F:20, (FILE, NOTO20), (OUTIN)
ASSIGN F:25, (FILE, NOTO25), (OUTIN)
ASSIGN F:35, (FILE, DESIGN), (IN)
ASSIGN F:35, (FILE, COMON1), (OUT), (SAVE)
ASSIGN F:40, (FILE, MGBWPDFE, GOULD), (IN)
ASSIGN F:45, (FILE, MGBWPDFM), (OUT), (SAVE)
ASSIGN F:55, (FILE, AWRKIX), (OUTIN), (SAVE)
ASSIGN F:50, (FILE, AWRKIX), (OUTIN), (SAVE)
ASSIGN F:60, (FILE, AWRKIX), (OUTIN), (SAVE)
                                                                                                           ...
  ASSIGN F:60, (FILE, AWRK2C), (OUTIN), (SAVE)
LASSIGN F:30,(FILE, DESIGN),(IN)
LASSIGN F:35,(FILE, COMON1),(OUT),(SAVE)
LASSIGN F:40,(FILE, COMON2),(IN)
LASSIGN F:45,(FILE, MGGMPDFE, GOULD),(IN)
LASSIGN F:50,(FILE, MGGMPDFM),(OUT),(SAVE)
LASSIGN F:55,(FILE, AWRK;X),(OUTIN),(SAVE)
LASSIGN F:60,(FILE, AWRK;C),(OUTIN),(SAVE)
  COPY CO15 TO LP
   END
 IRUN (LMN,LMPRZDC,GOULD)
 LDATA
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4 85 210
4 86 210
4 87 221
4 88 221
4 91 10
5 179450
5 189440
5 199800
TIMING CIRCUIT FOR JIM CURRIE, 2-X-76, REV 2-28-78, FELTNER - GOULD
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                       61330
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                      461330
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i	46	4	53	4	59	3	72	3										
i	49	3	55	3													-	
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1 79 3

IEOD

IPCL

DELETE NOTO10

DELETE NOTO20
```

DELETE NOTO25
DELETE DECODE
DELETE AWRK2C
END
1E00
END
PCL PROCESSING TERMINATED

RUN (LMN,LMPR2DC,GOULD)

### START OF INIT:

### TWO-DIMENSIONAL PLACEMENT & ROUTING OF LSI CHIPS.

WRITTEN BY: RICHARD NOTO
COMPANY : RCA CORP.
ADDRESS : FRONT & COOPER STS.
CAMDEN, N. J. 08102
PHONE NO. : (609) 963-8000
EXT. NO. : PC 6755

PR2D REVISION B, FEB. 1974.

10(10+	1)	_	50
IP(10+			1
+01)91	34)		1
IP(10+	53)	=	1
IP(10+	54)	*	3
IP(10+	68)	=	1
IP(12+	3)	=	200
19(12+	4)	=	50
16(15+	5)	=	50
IP(12+	(95	=	4
IP(14+	1)	=	1000
IP(14+	2)	=	1000
IP(14+	3)	=	50
IP(14+	9)	=	200
IP(14+	13)	=	300
IP(14+	14)	=	260
IP(14+	16)	*	120
IP(14+	17)	=	260
1P(14+	19)	¥	170
IP(14+	20)	=	150
IP(14+	55)	•	170
IP(14+	23)	=	100
IP(14+	26)	=	1

IP(I4+27) = 0

IPCI4+	30)		100
'IP(14+	31)	=	100
IP(14+	33)	•	100
IPCI4+	47)	=	17
IP(14+			
IP(14+	50)	*	14
IP(14+	68)	=	14
IP(14+			210
IP(14+	84)	=	210
IP(14+	85)		210
IPCI4+			
1P(14+	87)		221
IP(14+	88)	=	/Ž21
IP(I4+			-
1P(15+			
IP(15+	16)	=	9440
IP(15+	19)		9800

END OF INIT. START OF INPUT FUNCTION:

# PIN DATA FOR PATTERNS USED ON CHIP:

### PATTERN' 1120

PIN NO.	RESN Flag	X DIST	PIN IMP.
1	0	0	0
2	10	12	820
3	1	31	1300
4	1	50	1300
-	_ <u>.</u> ii	4.0	4.00

# PATTERN 1130

PIN	RESN	×	PIN
NO.	FLAG	DIST	IMP.
1	0	0	0
2	10	10	1180
3	1	29	1710
4	1	48	1710
5	1	67	1710
6	-6	A 1	140

### PATTERN 1220

PIN	RESN	X	PIN
NO.	FLAG	DIJT	IMP.
1	0	0	0
2	10	13	1040
3	1	32	1170
4	1	51	1170
5	-4	4.3	140

# PATTERN 1230

PIN	RESN	X	PIN
VO.	FLAG	DIST	IMP
1	0	0	0
2	10	10	500
3	1	29	1000
4	1	48	1000
5	1	67	1000
6	-6	77	140

TIMING CIRCU	17 FC	R JIm	LUHRIE	, 2-X-76,	REV 2-28-78	PAGE NO. 2
PIN DATA FOR PATT	ERNS	USED C	N CHIF	1 .		e e e e e e e e e e e e e e e e e e e
PATTERN	124		-			
	PIN NO.	RESN Flag	X DIST	PIN IMP.		
	1 2 3 4 5 6 7	10 1 1 1 1 1 -8	0 10 29 48 67 86	0 850 1540 1540 1540 1540		
PATTERN	131	0				
	PIN NO.	RESN Flag	X DIST	PIN IMP.		
	1 2 3 4	0 10 1 -2	0 10 29 39	0 1200 1800 140	·	
PATTERN	133	0				- · · · · <del>-</del>
	PIN NO.	RESN Flag	X DIST	PIN IMP.		
	1 2 3 4 5 6	0 1 10 2 3 -6	0 13 32 51 70 85	0 1370 1340 1370 2320 140		
PATTERN	152	0				
	PIN NO.	RESN Flag	X DIST	PIN IMP.		
	1 2 3	0 10 1	0 10 29	0 1790 3940 140		

IIMING CIRCA	IT FO	R JIM	CURRIE	, 2-x-76	, REV 2-28-78	PAGE NO. 3
PIN DATA FOR PATT	ERNS	USED (	N CHIE	<b>':</b>		<u></u>
				****		
PATTERN	162	0				
	PIN NO.	RESN Flag	X D1ST	PIN IMP.		
	1 2 3 4 5	0 10 1 1	0 13 32 51 63	0 1270 1170 1170 140		
PATTERN	172	0				
	PIN NO.	RESN Flag	X D181	PIN IMP.		
	1 2 3 4 5	0 10 1 1	0 12 31 50 60	0 1270 1310 1310 140		
PATTERN	182	0				
	PIN NO.	RESN FLAG	DIST	PIN IMP.		
	1 2 3 4 5	0 1 2 10 -14	0 30 49 73 84	0 500 1000 350 140		
PATTERN	183	0				
	PIN NO.	RESN Flag	DIST	PIN IMP.		<del></del>
	1 2 3 4	0 1 2 10	0 30 49 73	0 1500 600 1200	- · · -	

TIMING CIRCUI	T FO	R JIM	CURRIE	, 2-X-76	, REV 2-28-78	PAGE NO. 4
N DATA FOR PATTE	RNS	USED O	N CHIP	1		
PATTERN	231	0				
	PIN NO.		X DIST	PIN IMP.		
	1 2 3 4 5	10 1 1 1 -8	0 13 32 51 80	0 900 1550 1550 140		
PATTERN	902	0				
		RESN FLAG	X DIST	PIN IMP.		
	1 2 3	0 1 0	0 12 76	0 300 80		
PATTERN	903	0				
	PIN NO.	RESN Flag	X DIST	FIN- IMP.		
	2 1	0 10	0 12 76	0 300 80		

TIMI	NG CIR	CUIT	FOR .	JIM C	URRIE,	2-x-	76, R	EN S	-28-78			PAGE	NO.	5	
ELEMENT T ELEM	O PATT PTRN				PTRN	ORD.	ROW	•••							
. 1	1520	0	0	2	1520	0	0	3	1520	0	0	4	1620	0	0
5	1330	0	0	6	1330	0	0	7	1330	0	0	8	1330	0	0
9	1330	0	0	10	1330	0	0	11	1330	0	0	12	1330	0	0_
.13	1330	0	0	14	1330	0	0	15	1330	0	. 0	16	1330	0	0
17	1820	0	0	18	1820	,0	0	19	1820,	0	0	20	1820	0	0
21	1820	0	0	22	1820	0	0	23	1820	0	0	24	1820	0	0
25	1820	0	0	26	1820	0	0	27	1820	0	0	28	1820	0	0
29	1120	0	0	30	1120	0	0	31	1120	0	0	32	1120	0	0
33	1120	0	0	34	1120	0	0	35	1120	0	0	36	1120	0	. 0.
37	1120	0	0	38	1120	o	0	39	1120	0	0	40	1120	0	0
41	1520	ũ	0	42	1820	0	0	43	1720	0	0	44	1240	0	0
45	1330	0	0	46	1330	0	0	47	1330	0	0	48	1330	0	0
49	1,330	0	0	50	1330	0	0	51	1820	0	0	52	1820	0	0
53	1820	o	0	54	1-820	0	0	55	1820	0	0	56	1820	0	0
57	1120	0	0	58	1120	0	0	59	1120	0	0	60	1120	0	0
61	1120	0	0	62	1120	0	0	63	1520	0	0	64	1850	0	0
65	1550	0	0	66	1120	0	0	67	1220	. 0	0	68	1310	Ō	0
69	1520	0	0	70	2310	0	0	71	2310	0	0	72	2310	0	0
73	2310	0	0	74	2310	0	0	75	2310	0	0	76	1130	0	0
77	1130,	0	0	78	1220	0	. 0	79	1520	0	0	80	1310	0	0
85	1520	.0	0	82	1820	0	0	83	1720	. 0	0	84	1120	0	0
A5	1620	0	۸	86	1830	0	۸	a.v	1830	٥	n		1830	0	۸

TIMING CIRCUIT FOR JIM CURRIE, 2-X-76, REV 2-28-78

PAGE NO. 6

ELEMENT TO PATTERN ASSIGNMENT: ELEM PTRN ORD. ROW ELEM PTRN ORD. ROW ...

													· - · · - · ·		
89	1830	0	0	90	1830	0	0	91	1830	0	0	92	9030	0	0
93	9020	0	0	94	9020	0	0	95	9030	0	0	96	9030	0	. 0
97	9030	_0	0	98	9030	0_	. 0	99	9030	<u>o</u> .	0	100	9030	0	0
101	9030	ò	0	102	9020	0	0	103	9030	0	0	104	9020	0	0 ,
105	9020	0	0	106	1120	0	0	107	1120	0	0	108	1310	0	0
109	1720	<u> </u>	0	110	1650	0	0	111	1520	. 0	. 0	112	1520	0	0
113	1520	0	0	114	1230	0	0	115	1120	0	0	116	1520	0	0
117	1520	0	0	118	1520	0	0	119	1520	0	0	120	1820	0	0
121	1820	0	0	122	1520	0	. 0	123	1520	0_	0_	124	1520	0	0
125	1520	0	0	126	1520	0	0								

	TIMIYO	CIR	CUIT	FOR J	IM CUN	RIE,	2-x-7	6, RE	v 2-2	8-78		P	AGE N	0.	7							
MODE	LIST: NODE NO.		FLEM	PIN	ELEM	PIN	ELF"	PIN	ELE#	PIN												
	į	1	1 1 2	2 3 2	112	3 2 3	94 93 42	2 3	64	3												
	4	1	3	2	4	3	82	5	120	3	121	3										
	5	1	4	2	118	2	67 86	3	87	3	88	3	89	3	90	3	91	3				
	7 H	1	5 11	5	12	5 5	15	5	14	2	9 15	5	10 16	5	124 123	5						
	10	1	5	3	17	3																
	11	1	7	3	19	3																
	13	1	9	3	53	3																
	14	1	10 11	3	23	3																
	16	1	13	5 3	24 25	3 5																
	14	1	14 15	5 5	26 27	3																
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	24	1	H 9	4	19	4	31 32	3	114	3												
	26 21	í	10	5	10	5	11	5	12	5	33	4	34	4	35	4	36	4	116	2		
	54	i	11	4	22	4	34	3														
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	5 t	1	15 16	4	26 27	4	58 39	3	44	4												,
	55 56	1	17 18	5	29 30	5																
	37 38	1	19 20	5	31 32	5																
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	41	1	23	5	35 36	2																
	42	1	25	5	57	5																
	45	1	26 27	5	34	5																
	46 47	1	28 28	2 4	40 40	3	44	6	45	4												
	4A 49	1	4 1 4 1	2	113	3	116	3	117	3												
	50 51	1	42 43	3	43 115	5																
	52	1	64	2	43	4	65	2														

TIMING CIRCUIT FOR .	JIM CURRIE.	2-X-76. REV	2-28-78	PAGE NO.	8

NODE	LIST: NODE NO.	NODE	ELEM	PIN	ELEM	PIN	ELEM	PIN	ELEM	PIN	•••								
	59	1	47	3	53	3													
	60	1	47	4	52	4	58	3	71	3									
	61	1	48	3	54	3													
	62	1	48	4	53	4	59	3	72	3									
	63	1	49	3	55	3		7		,									
	64 65	1	49 49	4	54 50	4	60 61	3 4	73 62	3	48	5	60	4	126	z		120	2
	66	i	50	3	56	3	01	7	Q2	7	40	,	80	-	120	-	•	120	•
	67	î	50	4	55	4	61	3	74	3									
	68	i	56	4	65	3	75	3		-									
	69	ī	51	Ž	57	2		-											
	70	1	52	2	58	5													
	71	1	53	2	59	5													
	72	1	54	2	60	2													
	73	1	55	2	61	2													
	74	1	56	5	62	S		_											
	75	1	63	5	66	3	125	3	126	3									
	76 77	1	63	3	64 109	2	110	3											
	78	1	65 65	4	78	2	110	,											
	79	i	66	ž	106	3													
	80	i	66	4	106	ž	109	3											
	81	i	67	Ž	69	3	. •	-											
	82	ī	68	ž	118	3										-	٠		
	83	i	68	3	80	3	82	4											
	84	1	69	3	84	3	102	2											
	85	1	70	2	76	5													
	86	1	70	4	86	4													
	87	1	71	2	76	4													
	88	1	71	4	87	4													_
	89	1	72	2	76	3													
	90	1	72	4	88	4 5													
	91 92	1	73 73	2	77 89	<u>ح</u>													
	93	1	73 74	2	77	4													
	94	í	74	4	90	4													
	95	i	75	ž	77	3													
	96	i	75	4	91	4													
	97	1	76	2	78	4													
	98	1	77	2	78	3													
	99	1	79	5	105	5	81	3											
	100	1	81	2	104	2													
	101	1	82	2	108	5													
	102 103	1	83	2	85 107	3	108 84	3 4											
	103	1	83 83	4	107	2	85	2											
	105	i	84	2	107	3	0.5	£											
	106	i	85	4	103	ž										-			
	107	i	86	ž	101	ž													
	108	i	87	ž	100	5													
	109	ī	88	ž	99	ž													
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111	1	90	2	97	2		
112	1	91	2	96	2		
113	Í	92	2	Ш	3		
114	1	110	4	95	2		
115	1	106	4	109	4	110	2
116	1	111	2	112	3		

	TIMIN	G CIR	CUIT	FOR J	IM ÇUR	RIE,	2-X-7	6, REV 2-2	8-78	PAGE NO.	9
NODE	LIST: NODE NO.	NODE	ELEM	PIN	ELEM	PIN	ELEM	PIN ELEM	PIN		
	117	1	114	2	115	3					
	118	1	121	4	122	3					
	119	1	122	2	123	3	124	3			
	120	1	119	3	120	4		=			
	121	- 1	70	1	80	2					

NODE ERRORS:

END OF INPUT FUNCTION.

TIMING CIRCUIT FOR JIM CURRIE, 2-X-76, REV 2-28-78,

PAGE NO. TO

START OF PRPLI SUBROUTINE:

END OF PRPLI SUBROUTINE. START OF PLACEMENT:

TIMING CIRCUIT FOR JIM CURRIE, 2-x-76, REV 2-28-78, PAGE NO. 11

LENGTH= 32, # INT.= 830, JRSP=2, TOTAL DIST.= 0, # GOOD INT.= 0, # EQU INT = 830

PLACEMENT:

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TIMING CIRCUIT FOR JIM CURRIE, Z-X-76, REV 2-28-78, PAGE NO. T2

LENGTH= 28, # INT.= 1660, JRSP=2, TOTAL DIST.= 0, # GOOD INT.= 0, # EQU INT = 1660

PLACEMENT:

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TIMING CIRCUIT FOR JIM CURRIE, 2-X-76, REV 2-28-78, PAGE NO. 13

LENGTH= 25, # INT.= 2490, JRSP=2, TOTAL DIST.= 0, # GOOD INT.= 0, # EQU INT = 2490 PLACEMENT:

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TIMING CIRCUIT FOR JIM CURRIE, 2-X-76, REV 2-28-78, PAGE NO. 14

LENGTH= 22, \* INT. = 3520, JRSP=2, TOTAL DIST. = 0, \* GOOD INT. = 0, \* EQU INT = 3320

PLACEMENT:

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TIMING CIRCUIT FOR JIM CURRIE, 2-x-76, REV 2-28-78, PAGE NO. 15

LENGTH= 19, # INT.= 4150, JRSP=2, TOTAL DIST.\* 0, # GOOD INT.= 0, # EQU INT = 4150 PLACEMENT:

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TIMING CIRCUIT FOR JIM CURRIE, 2-x-76, REV 2-28-78, PAGE NO. 16

LENGTH= 16, # INT. = 4929, JRSP=2, TOTAL DIST. = 0, # GOOD INT. = 6, # EQU INT = 4840

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TIMING CIRCUIT FOR JIM CURRIE, 2-X-76, REV 2-26-78,

LENGTH= 13, # INT.= 7926, JRSP=2, TOTAL DIST.= 0, # GOOD INT.= 63, # EQU INT = 6785

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TIMING CIRCUIT FOR JIM CURRIE, 2-X=76, REV 2-28=78, PAGE NO. 18

LENGTH= 10, # INT.= 10486, JRSP=2, TOTAL DIST.= 0, # GOOD INT.= 157, # EQU INT = 7833

PLACEMENT:

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TIMING CIRCUIT FOR JIM CURRIE, 2-x-76, REV 2-28-78, PAGE NO. 19

LENGTH= 7, # INT.= 14934, JRSP=2, TOTAL DIST.= 48, # GOOD INT.= 295, # EQU INT = 9104

PLACEMENT:

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TOTAL	WIRE	UNITS	ВY	NODE 1							
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TIMING CIRCUIT FOR JIM CURRIE, Z-X-76, REV Z-28-78, PAGE NO. ZO

LENGTH= 4, # INT.= 18675, JRSP=2, TOTAL DIST.= 421, # GOOD INT.= 427, # EQU INT = 9637

PLACEMENT:

9	0	0	98	0	0	0	99	0	0	0	0	0	0	0	0	0	100	0	0	0	101	0	0	0
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93	Ó	0	111	56	60	73	75	48	49	77	119	72	78	58	46	57	71	40	39	16	27	ō	Ô	95
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TIMING CIRCUIT FOR JIM CURRIE, 2-X-76, REV 2-28-78, PAGE NO. 21

LENGTH= '1, # INT. = 23734, JRSP=3, TOTAL DIST. = 1978, # GOOD INT. = 598, # EQU INT = 9896

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93	0	0	112	119	49	48	62	56	50	55	61	77	74	78	110	39	16	28	40	45	51	0	0	95
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TOTAL	W 7 D E	11N. T T O	- D V	NODE:

PLACEMENT:

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0	0	0	0	3	14	18	18	0	2
41	0	21	36	0	0	2	0	8	Ō
17	0	0	75	19	42	17	1	38	38
18	15	1	1	0	36	36	36	36	38
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- 23				<u> </u>				<u>-</u>		-				<u> </u>		<del>•</del>								

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12	9	5	16	6	50	27	23	4	1	
9	1	1	9	6	<b>6</b>		4	<u>5</u>	1	
20	6	9	16	24	55	2	6	2	Ž	
34	2	17	5	1 ·	<b>T</b>		· 1	<b>— 1</b>	1	<del></del>
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TIMING CIRCUIT FOR JIM CURRIE, 2-X-76, REV 2-28-78,	PAGE NO. 23
	START OF FIX1 SUBROUTINE:
	END OF FIX1 SUBROUTINE.

TIMING CIRCUIT FOR JIM CURRIE, 2-X-76, REV 2-28-78, PAGE NO. 24

START OF FIX1 SUBROUTINE:

ENC OF FIX1 SUBROUTINE.
START OF PLACEMENT:

TIMING CIRCUIT FOR JIM CURRIE, 2=X=76, REV 2=28=78, THE PAGE NO. TEST PA

0	0	0	98	0	0	0	99	0	0	0	0	0	0	0	0	0	100	0	0	0	101	0	0	0
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0	0	0	111	73	89	0	88	0	72	53	59	47	58	76	71	52	87	46	70	57	86	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
93	0	0	0	77	119	48	49	62	56	50	61	55	74	0	110	39	40	28	16	45	51	0	Ó	95
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TOTAL	WIDE	UNITS	RΥ	NUDE
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32	27	29	46	64	65	2	26	2	2
84	2	47	27	1	1	1	1	1	1
1	1	1	1	24	1	27	33	27	1
23	8 ړ	8	39	1	106	2	17	2	4
24	25	5	48	30	1	3	26	15	3 .
1	1	1	1	5	21	35	51	1	5
28	1	29	50	5	2	1	2	5	2
16	1	9	82	30	49	97	75	52	52
1	46	2	2	1	48	48	48	48	52
44	20	44	20	25	74	20	1	26	24
21						_			

ACTU	AL WIRE U	NITS =	963	963						
	* 04				,			-		
TOTAL W	IRE UNITS	BY NODE:								
15	15	4	16	6	51	30	21	4	1	
11	1	1	8	5	5	1	5	2	. 1	
22	7	9	16	24	25	2	6	2	2	
34	. 2	17	7	1	1	, <b>1</b>	1	1	<u> </u>	
1	1	1	1	4	i	. 7	23	7	i	
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11								END C	F PLACE	MENT.

—	TIMING C	INCUIT FO	R JIM CUR	RIE, 2-1	X-76, REV	2-28-78,		PAGE	NO. 26		
								START	OF POST-PLAC	CE SUBROUTINE:	
ORIG	IN	•									
- +						<i>:</i>				<u></u>	
×		Y D 1	MENS	T O N	>						77
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	3	4	5		6	7		8	9	10	11
	ROW # 1:										
	92		94			-			93	VDD	
								112	78	•	
x	98	111			1	114		7	85	103	
		73	77		65	2		29	83.		
D,		89	119		120	64		17	107		
I		•	48		54	43		6	84		
м	99	88	49		60	42		18	69	102	
E			62		63	3		30	19		
N		72	56		115	108		5	31		
s		53	50		75	82		41	8		
I		59	_61 ~	•	126	4	•	67	20		
0		47	55		125	122		124	32		
N		58	74		66	121		68	113		
		76			106	123		118	116		
		71	110		109	14		80	9		
1		52	39		27	37		26	51 .		
ı	100	87	40		44	25		38	33	104	

	46	28	15	13	117	10	
	70	16	35	36	34	81	
	57	45	23	24	55	79	
101	56	51	12	11	91	90	105
		•					•
GND		95			9	6	97

ROW # 2.

END OF POST-PLACE SUBROUTINE.

TIMING CIRCUIT FOR	JIM CURRIE, 2-X-	76, REV 2-28-78,	PAGE NO. 27
		e e e e e e e e e e e e e e e e e e e	START OF ROUTE FUNCTION
			START SMASHIE

CELL RE-ORIENTATION / PIN RE-ASSIGNMENT:

ELEMENT NUMBER	ROW / ORIENT.	PIN	RE-ASSIGNMENT CHANGED TO
2	<b>-</b> 7		
3	<b>-7</b>		
5	-8		
6	<del>-</del> 8		
8	-9		
9	<b>-</b> 9		
11	<b>-</b> 7		
15	<b>-</b> 6		
16	<b>-</b> 5		
18	-8		
19	-9		
29	-8		
36	<del>-</del> 7	7	0
		3	4
37	<b>-</b> 7		
40	<b>-</b> 5		
41	<del>-</del> 8		
43	<del>-</del> 7		
44	6	3	5
		4 5	6
		6	4
47	-4,		
49	•5		
53	-4		
57	-4		

58 -4 61 -5

62 -5

TIMI	NG CIRCUI	T'FOR JIM CUR	RIE, 2-X-	76, REV 2-2	8-75,	PAGE NO.	59
CELL	RE-ORIEN	ITATION / PIN	RE-ASSIGN	MENT:			
•	ELEMENT NUMBER	ROW / ORIENT.		ASSIGNMENT Changed to		· <del></del>	
	63	<b>-</b> 6					
	64	<b>-7</b>			•		
=	65	6				···	
	•		3 4	4 3			-
	66	<b>-</b> 6					-
		-	3 4	3			
	72	-4					
	77	5	_	_			
			3 4 5	4 5 3			
	80	-8	,	-			
	83	9					
			3 4	4 3			
	84	9					
			4	4 3			
	85	<b>=</b> 9					
	86	-4					
	87	-4					· · · ·
	89	-4					
	107	<del>-</del> 9					
	108	<b>-7</b>				· - · - · · · · · · · · · · · · · · · ·	
	116	-9					
	118	<b>-</b> 8					
	119	=5		•	· · · · ·		
	. 150	- ·· · ·-6· —- ·		- ••			
	121				··· ·	<del></del>	

123 -7	END SMASH1.
START OF CENTER ROUTING:	
END OF CENTER ROUTING. START OF SIDE ROUTING:	·
END OF SIDE ROUTING.	END ROUTE FUNCTION.

PAGE NO. 30 START OF ART FUNCTION:

PAGE NO. 31

START ANALC1 END ANALC1.

PAGE NO. 32

START ANALS1 END ANALS1.

PAGE NO. 33

START PWR1 END PWR1.

PAGE NO. 34

START SORTI END SORTI.

PAGE NO. 35

START ARTI

TIMING CIRCUIT FOR JIM-GURRIE, 2-X-76, REV 2-28-78, PAGE NO. 36

ARTWORK INSTRUCTION DATA:

END ART1.

CHIP STATISTICS:

X-STEP = 213, Y-STEP = 169

6.46 INCHES OF METAL, .90 INCHES OF TUNNEL, AND 379 TUNNEL ENDS.

CELL ROW: LINEAR MILS: TOTAL LINEAR MILS:

4 129.4
5 126.4
6 126.1
7 127.4
8 125.1
9 127.2

761.6

CHIP AREA CFLL --- AREA R.P. --- AREA WIRING -- AREA WIRING EFFECT SQ. MILS 0/0 SQ.

PATTERN NUMBER TIME NUMBER DEVICES USED	
1120 4 23	•
1130 6 2	!
1220 4 3	•
1230 6 1	
1240 8 1	
1310 2 3	•
1330 6 18	•
1520 2 20	•
1620 6 3	<b>i</b>
1720 6 3	<b>S</b>
1820 14 23	<b>S</b>
1830 10 6	•
2310 8 6	•
9020 0 5	i
9030 0 9	)

	TIMING	CIRC	UĮT F	OR JIM	CUR	RIE, A	2-x-76	6, REV	2-2	5-75,			PAGE	NO.	39							
NODE	LIST: NODE NO.	CAP.	NODE WT.	ELEM	PIN	ELEM	PIN	ELFM	PIN	•••												
	1 2 5 4 5 6 7 A 9 10 11 1 1 2 1 3 1 4 5 1 5 1 7 1 1 4 1 9 1 9 1 9 1 9 1 9 1 9 1 9 1 9 1	180 283 68 336 625 625 529 163 23 174 163 23 174 1156 136 39	2 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1 112 2 3 4 118 124 173 5 6 7 8 9 10 11 12 13 14 15 15	222222235555555555555555555555555555555	213454627 117819012234567	333544225533333333333333333333333333333	94 93 42 82 67 86 7 13	22334322	64 120 87 8 14	3 3 2 2 2	121 88 9 15	3 2 2	89 10 16	3 2 2	90 5 11	3 2 2	91	3			
	20 21 22 23 24 25 24 27 28	98 413 230 195 371 550 587 41 179	1 1 1 1 1 1 1 1 1 1 1 1	16 113 17 18 19 20 116 21	5 2 4 4 4 2 4 4 2 4 4	28 6 7 8 9 10 10	354444544	/ 29 50 51 32 11 33	5 5 5 5 5 5 5 5	8 114 114 114 44 12	5 4 3 5	29 33	4	30	4	31	4	36	4	5	5	
	29 30 51 52 55 54 35 56 57 58	82 50 57 50 589 193 67 87 13	1 1 1 1 1 1 1 1	25 24 117 25 26 27 29 30 31	442444255	12 13 14 14 15 16 17 18 19 20	4 4 5 4 4 5 5 5 5 5	35 36 15 37 5H 39	3 11 5 3 3 3 3	16 44 44	5 6 3	37	4	38	4	39	4	40	4	13	5	121
	50 51 52	42 45 20 38 47 95 87 1427 150 43 254 157		35 35 35 37 38 39 40 41 42 41 42 41 42 41 42 41 42 41 42 41 42 41 42 44 44 44 44 44 44 44 44 44 44 44 44	4 2 2 2	21 22 23 24 25 26 27 28 40 113 41 42 43 43 115	2222233344	44 110	4 3	45 117	4 3											

54 667 1 119 2 46 2 47 2 48 2 49 2 50 2 45 2 55 52 1 45 3 51 3 56 1031 1 125 2 46 5 57 4 58 4 67 3 47 5 59 4 45 57 107 1 46 3 52 3 58 177 1 51 4 46 4 57 3 70 3

	TIMING	CIRC	UIT F	OR JIM	CUR	RIE, Z	-x-7	6, REV	5-5	8-75,			PAGE	NO.	40				
NODE	LIST: NODE NO.	CAP.	NODE WT.	ELEM	PIN	ELEM	PIN	ELEM	PIN										
	59	83	1	47	3	53	3												
	60	140	1	52	4	47	4	58	3	71	3								
	61 62	109 282	1	48 53	3	54 48	3	59	3	72	3								
	63	105	i	49	3	55	3	•	-	, -	•								
	64	316	1	54	4	49	4	60	3	73	3	_	_			_	_		
	65	355	1	126	Š	50	5	61	4	65	4	48	5	60	4	49	5	120	2
	66 67	80 147	1	50 55	3	56 50	3 4	61	3	74	3								
	68	176	i	56	4	62	3	75	3	, -	_								
	69	103	1	57	2	51	2												
	70	119	1	58	2	52	2												
	71 72	38 82	1	59 60	5	53 54	5												
	73	47	i	61	ž	55	5												
	74	77	1	62	2	56	2				_								
	75	120	1	63	5	66	4	125	3	126	3								
	16 77	223 252	1	64 109	4	63 65	3 4	110	3										
	78	590	i	78	5	65	3	•••	•										
	79	57	1	66	2	106	3												
	80	83	1	106	5	66 69	3	109	3										
	81 82	316 15	1 1	67 68	5	118	3												
	83	171	i	82	4	80	3	68	3										
	84	69	5	69	5	84	4	102	2										
	85	76	1	70 86	2	76 70	5												
	86 87	· 77	1	71	5	76	4												
	88	96	i	87	4	71	4												
	89	70	1	72	5	76	3												
	90	16 90	1	88 73	2	72 77	4												
	91 92	16	1	89	4	73	4												
	93	126	i	74	2	77	5												
	94	450	1	90	4	74	4												
	95 96	175 404	1	75 91	2	77 75	4												
	97	628	i	76	5	78	4												
	98	388	1	77	2	78	3												
	99	68	2	79	5	105	5	81	3										
	100 101	94 11	2	81 108	5	104 82	5												
	102	353	i	83	ž	85	2	108	3										
	105	105	1	107	5	83	4	84	3										
	104	139 87	1	85 84	5	107 107	4	83	3										
	105 106	71	Ş	103	2	85	4												
	107	70	2	101	2	86	2												
	108	52	2	100	5	87	2												
	109	69	5	99	5	88 89	5												
	110 111	38 136	5	98 97	2	90	2												
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TIMING CIRCUIT F	OR JIM CURRIE, 2-X-	76, REV 2-28-78,	PAGE NO. 52
NODE CROSSOVER C	DUNT:		
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	FOR JIM CURRIE, 2-X-70	b, KEV 2-28-78,	PAGE NO.	55
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		X-76, REV 2-28-78,	PAGE NO.	56
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	104	17		
	105	12		<del></del>

TIMING CIRCUIT FOR JIM CURRIE, 2=x= Node Crossover Count:	10, KEV C=28=75,	PAGE NO. 57
NODE	NUMBER	
NUMBER	CRUSSOVERS	
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120	11	
	16	END OF PLOT.

TIMING CIRCUIT	FOR JIM CURRIE,	2-X-76, REV 2-28-78,	PAGE NO. 38
			START OF WRITE FUNCTION.

TIMING CIRCU	IT FOR JIM (	CURRIE, 2	-X-76, REV	2-28-78,		AGE NO.	59			
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52	17	5	5184	4449	0	4	64
53	11	5	4818	4449	0	-4	64
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40   <b>30</b>	112	3	1191	1	114	,	1 114		2 115 3 26	3

\*STOP\* 0

PCL
DELETE NOTO10
.. 1 FILES DELETED, 305 GRANULES
DELETE NOTO15
.. 1 FILES DELETED, 305 GRANULES
DELETE NOTO20
.. 1 FILES DELETED, 4 GRANULES
DELETE NOTO25
.. 1 FILES DELETED, 140 GRANULES
DELETE DECODE
.. 1 FILES DELETED, 1 GRANULES
DELETE AWRK2C
.. 1 FILES DELETED, 9 GRANULES
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09:56 JUL 13,778 ID=068A			
ELAPSED JOB TIME	01:24:00		
PARTITION NUMBER	7		
TOTAL CPU TIME	14.3803		
PROCESSOR EXECUTION TIME	.0121		
PROCESSOR SERVICE TIME	.0494		
USER EXECUTION TIME	13,4790		
USER SERVICE TIME	.8399		
CARDS: CARDS READ	231		
PAGES: PROCESSOR PAGES	11		
USER PAGES	139		
TAPES: DRIVES ALLOCATED	2		
CORE: PEAK CORE(PAGES)	112	•	
PAGE.MINUTES	1497		
I/O: OPERATIONS	4040		•
CALS	35036		
FILE SPACE			
AVLBL RAD PERMANENT	64		
NET DISK PERMANENT	154		
AVLBL DISK PERMANENT	2834		
NUMBER OF SWAPS	78		
RESOURCES ALLOCATED			
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15. SUPPLEMENTARY NOTES

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16. ABSTRACT

Place, Route in 2-Dimensions (PR2D) is a standard cell automatic layout computer program for generating Large Scale Integrated/Metal Oxide Semiconductor (LSI/MOS) arrays. It is one of the components in the NASA/MSFC Computer Aided Design and Test system (CADAT). The program has been utilized successfully for a number of years in both Government and private sectors but until now has been undocumented. This material describes the compilation, loading, and execution of the program on a Sigma V CP-V operating system located at the NASA/MSFC Electronics and Control Facility. This material is also intended to aid in the conversion and running of the program on other data processing systems.

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